

Product Description: T315XW02 TFT-LCD PANEL									
AUO Model Name: T315XW02 VC									
Customer Part No/Project Name:									
	i								
Customer Signature	Date	AUO	Date						
Customer Signature	Date	AUO Approved By: Yu Chieh Lin	Date						
Customer Signature	Date								



Document Version: 3

Date: 2007/03/14

Product Specifications

31.5" WXGA Color TFT-LCD Module Model Name: T315XW02 VC

() Preliminary Specifications (*) Final Specifications



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Record of Revision

Version	Date	No	Old Description	New Description	Remark
0	2006/08/30		First issue		
1	2007/01/10	7/11		Update B/L power consumption min/max value	
1	2007/01/10	23		Add Thermal Shock Test Criteria	
2	2007/02/08	7/11	Power consumption typ=115W (4.6A)	Update Power consumption typ = 105W (4.375A)	
3	2007/03/14	12	Horizontal section period min = 1414	Horizontal section period min = 1440	
3	2007/03/14	11		Update inverter pin assignment pin 11~14	



1. General Description

This specification applies to the 31.5 inch Color TFT-LCD Module T315XW02 VC. This LCD module has a TFT active matrix type liquid crystal panel 1366x768 pixels, and diagonal size of 31.51 inch. This module supports 1366x768 XGA-WIDE mode (Non-interlace). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T315XW02 VC has been designed to apply the 8-bit 1 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

* General Information

Items	Specification	Unit	Note
Active Screen Size	31.5 inches		
Display Area	697.685 (H) x 392.256(V)	mm	
Outline Dimension	760.0(H) x 450.0(V) x 45(D)	mm	With inverter
Driver Element	a-Si TFT active matrix		
Display Colors	16.7M	Colors	
Number of Pixels	1366 x 768	Pixel	
Pixel Pitch	0.51075	mm	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Surface Treatment	AG, 3H		



2. Absolute Maximum Ratings

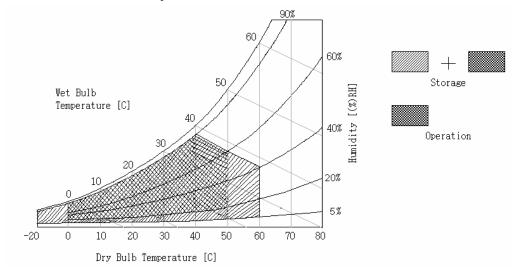
The following are maximum values which, if exceeded, may cause permanent damage to the unit.

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	7.0	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	3.6	[Volt]	Note 1
BLU Input Voltage	VDDB	-0.3	28	[Volt]	Note 1
BLU Brightness Control Voltage	Vdim	-0.3	7.0	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	НОР	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	

Note 1: Duration:50 msec.

Note 2: Maximum Wet-Bulb should be 39°C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C , the wet bulb temperature must not exceed 39°C .





3. Electrical Specification

The T315XW02 VC requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the BLU, is to power inverter..

3-1 Electrical Characteristics

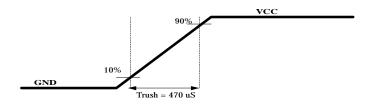
P	arameter			Valu	Unit	Notes	
			Min	Тур	Max		
LCD:							
Power	Supply Input	Vcc	4.5	5.0	5.5	Vdc	1
Voltage							
Power	Supply Input	Icc	-	1.2	1.5	Α	2
Current							
Power (Consumption	Pc	-	6.0	7.5	Watt	2
Inrush (Current	I_{RUSH}	-	-	6	Apeak	3
LVDS	Differential Input High	VTH			+100	mV	4
Interface	Threshold Voltage						
	Differential Input Low	VTL	-100			mV	4
	Threshold Voltage						
	Common Input	VCIM	1.10	1.25	1.40	V	4
	Voltage						
CMOS	Input High Threshold	VIH	2.4		3.3	Vdc	
Interface	Voltage	(High)					
	Input Low Threshold	VIL	0		0.7	Vdc	
	Voltage	(Low)					
Backlight	Power	PDDB	99	105	111	Watt	5,6
Consumpt	ion						
CCFL Life	e Time			60,000		Hours	7

Note:

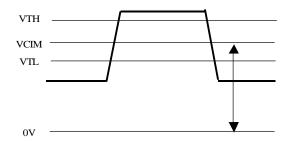
- 1. The ripple voltage should be controlled under 10% of V_{CC}
- 2. Vcc=5.0V, f_v = 60Hz, fCLK=81.5Mhz , 25°C , Test Pattern : White Pattern



3. Measurement condition :



4. VCIM = 1.2V



- 5. The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.
- **6.** Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.
- 7. The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C , the wet bulb temperature must not exceed 39°C . When operate at low temperatures, the brightness of CCFL will drop and the life time of CCFL will be reduced.



3-2 Interface Connections

- LCD connector (CN3): JAE FI-E30S or equivalent.

- LVDS Transmitter: DS90C385 (NS) or equivalent

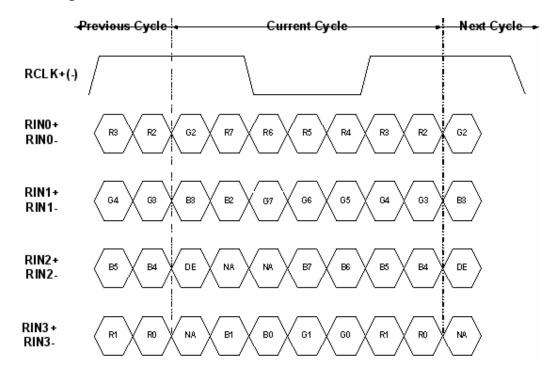
Pin No	Symbol	Description	note
1	Reserved	Open or High	Auo internal test pin
2	Reserved	Open or High	Auo internal test pin
3	Reserved	Open or High	Auo internal test pin
4	GND	Ground and Signal Return	
5	RIN0-	LVDS Channel 0 negative	
6	RIN0+	LVDS Channel 0 positive	
7	GND	Ground and Signal Return for LVDS	
8	RIN1-	LVDS Channel 1 negative	
9	RIN1+	LVDS Channel 1 positive	
10	GND	Ground and Signal Return for LVDS	
11	RIN2-	LVDS Channel 2 negative	
12	RIN2+	LVDS Channel 2 positive	
13	GND	Ground and Signal Return for LVDS	
14	RCLK-	LVDS Clock negative	
15	RCLK+	LVDS Clock positive	
16	GND	Ground and Signal Return for LVDS	
17	RIN3-	LVDS Channel 3 negative	
18	RIN3+	LVDS Channel 3 positive	
19	GND	Ground and Signal Return	
20	Reserved	Open or High	Auo internal test pin
21	LVDS Option	Low for JEIDA, High/Open for NS	
22	Reserved	Open	
23	GND	Ground and Signal Return	
24	GND	Ground and Signal Return	
25	GND	Ground and Signal Return	
26	Vcc	5V, DC, Regulated	
27	Vcc	5V, DC, Regulated	
28	Vcc	5V, DC, Regulated	
29	Vcc	5V, DC, Regulated	
30	Vcc	5V, DC, Regulated	

Note:

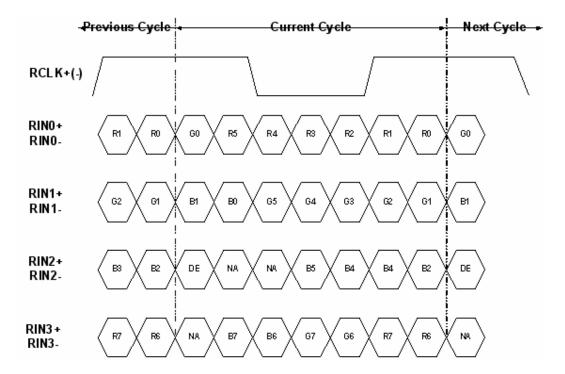
1. All GND (ground) pins should be connected together and should also be connected to the LCD's metal frame. All Vcc (power input) pins should be connected together.



LVDS Option = L(GND)



LVDS Option = H(3.3V) or Open





BACKLIGHT CONNECTOR PIN CONFIGURATION -

1. Electrical specification

Item	Cross		Condition		Spec		Unit	Mass
item	Symb.		Condition	Min	Тур	Max	Oilit	Note
Input Voltage	VDDB			21.6	24.0	26.4	VDC	
Input Current (Stable Condition)	IDDB		VDDB=24V	4.13	4.38	4.63	ADC	1
Input Power (Stable Condition)		VDDB=24V	99	105	111	W	1	
Inrush Current	IRUSH		VDDB=24V			6.0	ADC	1,2
On/Off Control Voltage	VBLON	ON	VDDB=24V	2.0		5.0	VDC	
On/On Control Voltage	VBLON	OFF	VDDB=24V	0.0		0.8	VDC	
On/Off Control Current	IBLON		VDDB=24V	0.0		1.5	mADC	
Dimming Control Current	VDIM MAX MIN		VDDB=24V		3.3		VDC	
Dimming Control Current			VDDB=24V		0.0		VDC	

Note1 : VDIM= 3.3V (MAX) (Ta= $25\pm5^{\circ}$ C, Turn on for 45 minutes)

Note 2 : Measurement condition Rising time = 20 ms (VDDB : 10%~90%);

2. Input specification

CN1: JST PHR-14 or equivalent

CIVI. JDI	Tilk-14 of equivalent	·
No	Symbol	Description
1	VDDB (Main Power)	DC input 24.0 VDC
2	VDDB (Main Power)	DC input 24.0 VDC
3	VDDB (Main Power)	DC input 24.0 VDC
4	VDDB (Main Power)	DC input 24.0 VDC
5	VDDB (Main Power)	DC input 24.0 VDC
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	GND	Ground
10	GND	Ground
11	DET	Panel status detect(0~0.8V=Normal, High=Inverter shut down)
12	VBLON (Enable Pin)	On/Off control Signal; High: On; Low: Off
13	VDIM (LCD Bright)	Internal PWM Dimming control signal input (DC 0~3.3V)
		(3.3V: Maximum brightness, 0V min brightness)
14	Reserved	Please leave it Open.



3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

* Timing Table

DE only Mode

Vertical Frequency:

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Notes
Vertical	Period	Τv	784	806	1015	Th	
Section	Active	Tdisp(v)		768		Th	
Section	Blanking	Tblk(v)	16	38	247	Th	
Horizontal	Period	Th	1440	1560	1900	Tclk	
Section	Active	Tdisp(h)		1366	Tclk		
Section	Blanking	Tblk(h)	48	194	534	Tclk	
LVDS Clock	Frequency	Fclk (1/Tclk)	60	80	85	MHz	
Vertical Frequency	Frequency		47	60	60 63		
Horizontal Frequency		Fh	43	48	53	kHz	

Notes:

1.) Display position is specific by the rise of DE signal only.

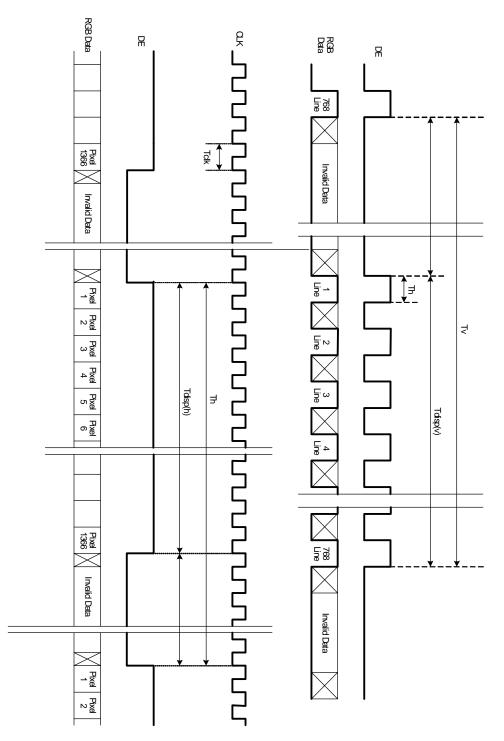
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise the of 1st DE is displayed at the top line of screen.

- 3.) If a period of DE "High" is less than 1366 DCLK or less than 768 lines, the rest of the screen displays black.
- 4.) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.



3-4 Signal Timing Waveforms





3-5 Color Input Data Reference

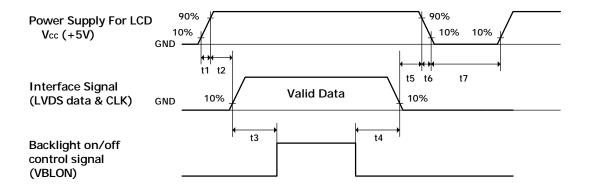
The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

		Input Color Data																							
Color					RI	ED							GRI	EEN							BL	UE			
		MS	В					Ι	SB	MS	В					I	SB	MS	В					I	LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	B2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED						 																			
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN						 																			
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE						<u></u>																			
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3.6.1 Power Sequence for LCD



Parameter			Units				
	Min.	Тур.	Тур. Мах.				
t1	0.4	-	20	Ms			
t2	0.5	-	50	Ms			
t3	500	-	-	Ms			
t4	10	-	-	Ms			
t5	0.5	-	50	Ms			
t6		-	300	Ms			
t7	1	-	-	S			

^{*1} If t3=200ms, input black signal till 700ms from system if necessary

In case of t3<200ms, the abnormal display will be happened. But it will not damage timing controller.

Note:

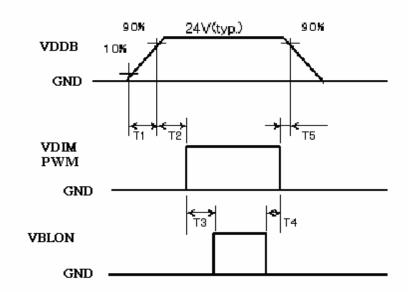
The timing controller will not be damaged in case of TV set AC input power suddenly shut down.

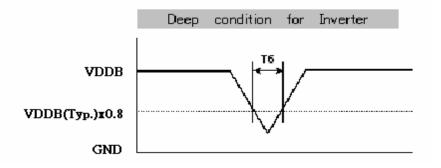
Once power reset, it should follow power sequence as spec. definition.

(1) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become abnormal screen.



3.6.2 Power Sequence for Inverter





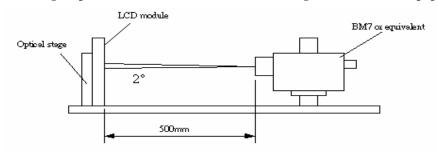
Parameter	Values			Units
	Min.	Тур.	Max.	
T1	20	-	-	Ms
T2	50	-	-	Ms
Т3	0	-	-	Ms
T4	0	-	-	Ms
T5	0	-	-	Ms
T6	-	-	10	Ms



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

Fig.1 1 presents additional information concerning the measurement equipment and method.



Parameter		Symbol		Values			Units	Notes
				Min.	Тур.	Max.		
Contrast Ratio		CR		1200	1500			1
Surface Luminance, white		LWF	I	400	500		cd/m²	2
Luminance Variation		δ_{white}	9 p			1.40		3
Response time	Gray to Gray	Τγ			8		ms	4
Color Gamut		NTS	 С		72		%	
Color Coo	ordinates							
	RED	$R_{\rm X}$			0.64			
		R_{Y}			0.33			
	GREEN	G_X			0.29	-		
		$G_{ m Y}$		T 0.02	0.60	T .0.02		
	BLUE	B_{X}		Typ0.03	0.15	Typ.+0.03		
		B_{Y}			0.06			
	WHITE	W_{X}			0.28			
		$W_{ m Y}$			0.29			
Viewing A	angle							
x axis, right(φ=0°)		θ_{r}			89		Degree	6
x axis, left(φ=180°)		θ_l			89			
y axis, up(φ=90°)		θ_{u}			89			
y axis, down (φ=0°)		$\theta_{\rm d}$			89			



Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\begin{aligned} \textbf{Contrast Ratio=} & \frac{\textbf{Surface Luminance of } L_{on1}}{\textbf{Surface Luminance of } L_{off1}} \end{aligned}$$

- 2. Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When VDDB = 24V, IDDB = 5A. L_{WH} =Lon1 Where Lon1 is the luminance with all pixels displaying white at center 1 location.
- 3. The variation in surface luminance, δWHITE is defined (center of Screen) as:

$$\delta_{WHITE(9P)}\!\!=Maximum(L_{on1},\,L_{on2},\ldots,\!L_{on9})/\,Minimum(L_{on1},\,L_{on2},\ldots L_{on9})$$

- 4. Response time is the time required for the display to transition from black to white(Rise Time, Tr_R) and from white to black (Decay Time, Tr_D). For additional information see FIG3.
- 5. Ty is the response time between any two gray scale and is based on f_v =60Hz to optimize.
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.

FIG. 2 Luminance

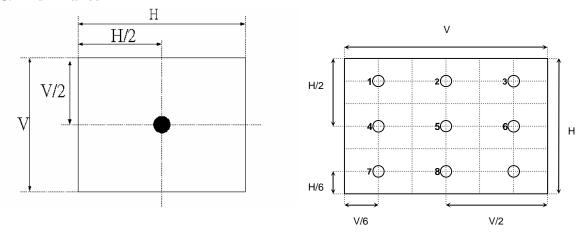




FIG.3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of gray(bright)" and "any level of gray(dark)".

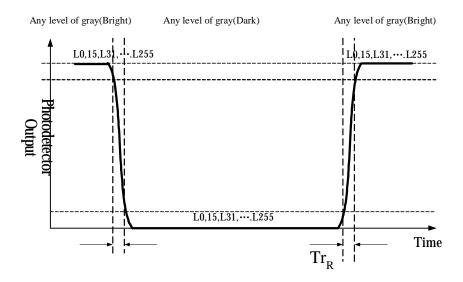
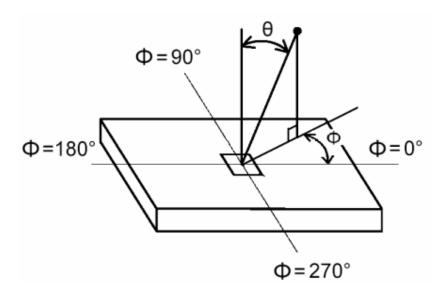


FIG.4 Viewing angle





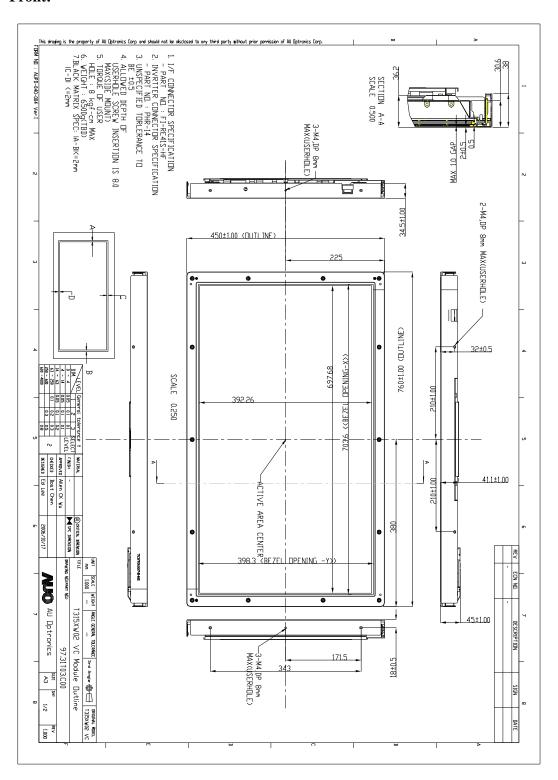
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T315XW02 V1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	760.0mm
Outline Dimension	Vertical	450.0mm
	Depth	45mm
Bezel Opening	Horizontal	703.6mm
	Vertical	398.3mm
Active Display Area	Horizontal	697.68mm
	Vertical	392.26mm
Weight	6500g Typ.	
Surface Treatment	AG, 3H	

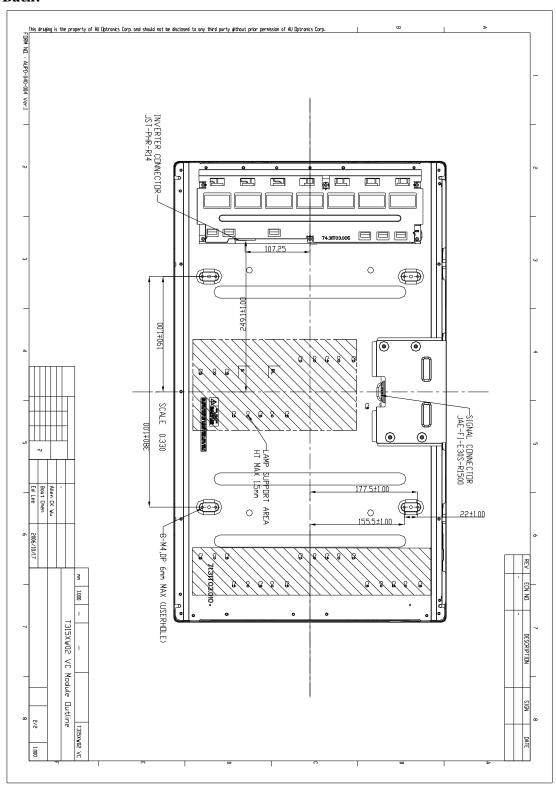


Front:





Back:





6. Reliability:

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta=60°C Determination:300h
2	Low temperature storage test	Ta=-20°C Determination:300h
3	High temperature operation test	Ta=50°C Determination:300h
4	Low temperature operation test	Ta=-5°C Determination:300h
5	Thermal Humidity Bias Test	Ta=50°C 80%RH Determination:300h
6	Thermal Shock Test	Ta=-20°C/0.5h~60°C/0.5h Determination:500cycles
7	Vibration test (non-operating)	Wave form: random Vibration level: 1.0G RMS Bandwidth: 10-500Hz Duration: X, Y, Z 20min, one time each direction
8	Shock test (non-operating)	Shock level: 50G Waveform: half since wave, 11ms Direction: ±X, ±Y, ±Z, one time each direction
9	Vibration test (with carton)	Random wave Vibration:10~200Hz,1.5Grms,30minutes Direction: ±X, ±Y, ±Z, one time each direction
10	Drop test (with carton)	Height: 53.3cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)



7. International Standard

7-1. Safety

(1) UL1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995

Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) CAN/CSA C22.2 No. 950-95/60950 Third Edition, Canadian Standards Association,

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

(3) EN60950: 1992+A2: 1993+A2: 1993+C3: 1995+A4: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+C3: 1995+A4:1996

European Committee for Electrotechnical Standardization (CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998



8. Packing

Panel label:



TW6562700014-ZMA00

Manufactured XX/XX
Model No: T315XW02 V.X
AU Optronics XXXXX
MADE IN XXXXXX (XX)





TW6562700014

TW65627: Production Lot 00014: Panel Serial Number ZMA: AUO internal code

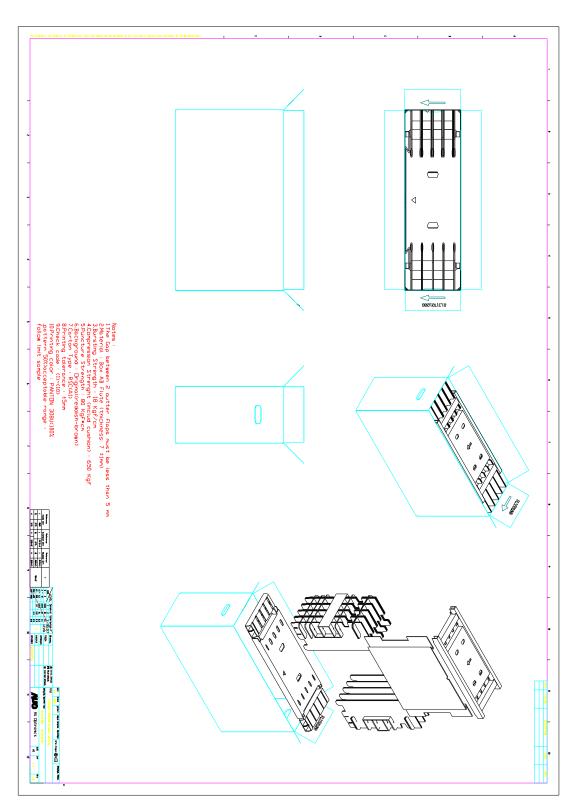
Manufactured 06/21: 2006 week 21 Made In Taiwan: Taiwan made

Carton Label:

AU ptronics QTY: X
MDDEL NO: T315XW02 VX
PART NO: 97.31T03.XXX
CUSTOMER NO:
CARTON NO:



Carton:





9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.



9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.