

SPECIFICATION FOR APPROVAL

- **(●)** Preliminary Specification
- () Final Specification

Title 47.0" WUXGA TFT LCD	
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BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LC470EUD		
SUFFIX	SCA1 (RoHS Verified)		

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE					
Diagon return 1 convetor year	r confirmation with					
Please return 1 copy for your confirmation with your signature and comments.						

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.0	Aug, 04, 2009	-	Preliminary Specification(First Draft)
0.1	Oct, 06, 2009	21, 22	Change the mechanical characteristic
0.2	Nov, 13, 2009	7	Update electrical specification
0.3	Nov, 16, 2009	24, 25	Update mechanical characteristic
0.4	Mar, 05, 2010	6	Update electrical characteristics
1.0	Mar. 16. 2010		CAS Version 1.0 Release
			Final Specification

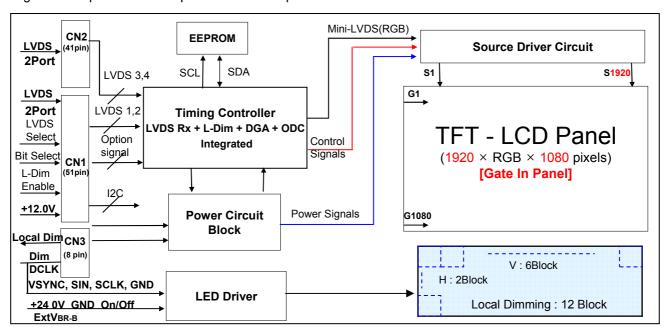
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1. General Description

The LC470EUD is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 46.96 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.06Bilion colors.

It has been designed to apply the 10-bit 4-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	46.96 inch (1192.87mm) diagonal			
Outline Dimension	1083.6(H) x 628.8(V) x 10.8(D) mm (Typ.)			
Pixel Pitch	0.5415 mm x 0.5415 mm			
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement			
Color Depth	10bit(D), 1.06Billon colors			
Luminance, White	450 cd/m² (Center 1point ,Typ.)			
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))			
Power Consumption	Total 110.7W (Typ.TBD) (Logic=8.4W, LED Driver=102.3W(ExtVbr_B=100%)]			
Weight	15.0 Kg (Typ.)			
Display Mode	Transmissive mode, Normally black			
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)			

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2. Absolute Maximum Ratings

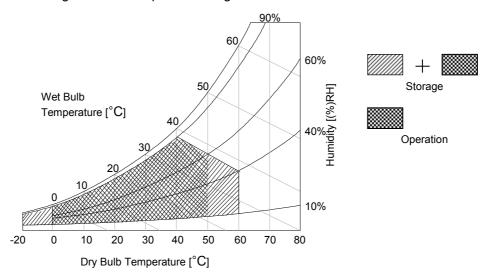
The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
		Syllibol	Min	Max	o iii	NOLE
Dower Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
Power Input Voltage	Driver	VBL	-0.3	+ 27.0	VDC	
Driver Central Voltage	ON/OFF	Voff / Von	-0.3	+5.5	VDC	1
Driver Control Voltage	Brightness	EXTVBR-B	0.0	+5.5	VDC	
T-Con Option Selection Voltage		VLOGIC	-0.3	+4.0	VDC	
Operating Temperature		Тор	0	+50	°C	2,3
Storage Temperature		Тѕт	-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	0.0
Storage Humidity		Нѕт	10	90	%RH	2,3

Note1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may degraded in case of improper thermal management in final product design.



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3. Electrical Specifications

3-1. Electrical Characteristics

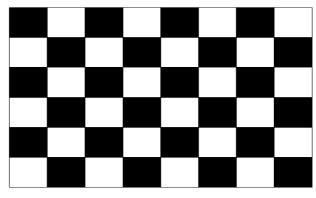
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note		
raidilletei	Symbol	Min	Тур	Max	Offic	Note	
Circuit :							
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC		
Devices Innovit Comment	ILCD	-	702	913	mA	1	
Power Input Current		-	980	1271	mA	2	
Power Consumption	PLCD		8.42	10.95	Watt	1	
Rush current	IRUSH	-	_	5.0	А	3	

- Note 1. The specified current and power consumption are under the V_{LCD} =12.0V, Ta=25 \pm 2°C, f_V =120Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
 - 2. The current is specified at the maximum current pattern.
 - 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

White: 1023 Gray Black: 0 Gray



Mosaic Pattern(8 x 6)

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Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol		Values			Notes		
		Syllibol	Min	Тур	Max	Unit	Notes		
LED Driver :									
Power Supply Inpu	wer Supply Input Voltage		VBL	22.8	24.0	25.2	Vdc	1	
Power Supply Input	t Current		IBL_A	-	4.27	4.54	Α	Ext VBR-B = 100%	
Power Supply Input Current (In-Rush)		Irush	-	-	5.73	A	V _{BL} = 22.8V Ext V _{BR-B} = 100% 4		
Power Consumption	on		PBL	-	102.3	108.9	W	Ext VBR-B = 100%	
	On/Off	0=/0#	On	V on	2.5	-	5.0	Vdc	
		Off	V off	-0.3	0.0	0.8	Vdc		
Innut Valtage for	Brightness	Adjust	ExtVBR-B	10	-	100	%	On Duty	
Input Voltage for Control System	PWM Frequ	uency for	PAL		100		Hz	3	
Signals	NTSC & PAL		NTSC		120		Hz	3	
	Pulse Duty	Pulse Duty Level		2.5	-	5.0	Vdc	HIGH : on duty	
(PWM)		Low Level	0.0	-	0.8	Vdc	LOW : off dutý		
LED:	LED:								
Life Time			30,000			Hrs	2		

Notes:

- 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (ExtVBR-B: 100%), it is total power consumption.
- 2. The life time(MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at $25\pm2^{\circ}$ C.
- 3. LGD recommend that the PWM freq. is synchronized with One time harmonic of Vsync signal of system. Though PWM frequency is over 120Hz (max 252Hz), function of LED Driver is not affected.
- 4. The duration of rush current is about 200ms.
- 5. Even though inrush current is over the specified value, there is no problem if I²T spec of fuse is satisfied.

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3-2. Interface Connections

This LCD module employs three kinds of interface connection, 51-pin, 41-pin and 8-pin connector are used for the module electronics and 14-pin connector is used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1) : FI-R51S-HF(manufactured by JAE) or KN25-51P-0.5SH(manufactured by Hirose)

Refer to below and next Page table

- Mating Connector: FI-R51HL(JAE) or compatible

Table 4-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC	No Connection	27	Bit Select	'H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	EXTV _{BR-B}	External VBR (From System)	34	GND	Ground
9	NC	No Connection	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	L-DIM Enable	'H' = Enable , 'L' or NC = Disable	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC	No Connection
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC	No Connection
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC	No Connection	-	-	-

Note

- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module. These pins should be no connection.
- 5. Specific pins(pin No. # 8~#10) are used for Local Dimming function of the LCD module.

 If not used, these pins are no connection. (Please see the Appendix III-4 for more information.)
- 6. LVDS pin (pin No. #24,25,40,41) are used for 10Bit(D) of the LCD module. If used for 8Bit(R), these pins are no connection.
- 7. Specific pin No. #44 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

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- LCD Connector (CN2): FI-RE41S-HF (manufactured by JAE) or KN25-41P-0.5SH (manufactured by Hirose)
- Mating Connector : FI-RE41HL

Table 4-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION

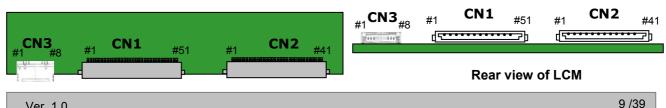
No	Symbol	Description	No	Symbol	Description
1	NC	No connection(Reserved)	22	RE3N	THIRD LVDS Receiver Signal (E-)
2	NC	No connection	23	RE3P	THIRD LVDS Receiver Signal (E+)
3	NC	No connection	24	GND	Ground
4	NC	No connection	25	GND	Ground
5	NC	No connection	26	RA4N	FORTH LVDS Receiver Signal (A-)
6	NC	No connection	27	RA4P	FORTH LVDS Receiver Signal (A+)
7	NC	No connection	28	RB4N	FORTH LVDS Receiver Signal (B-)
8	NC	No connection	29	RB4P	FORTH LVDS Receiver Signal (B+)
9	GND	Ground	30	RC4N	FORTH LVDS Receiver Signal (C-)
10	RA3N	THIRD LVDS Receiver Signal (A-)	31	RC4P	FORTH LVDS Receiver Signal (C+)
11	RA3P	THIRD LVDS Receiver Signal (A+)	32	GND	Ground
12	RB3N	THIRD LVDS Receiver Signal (B-)	33	RCLK4N	FORTH LVDS Receiver Clock Signal(-)
13	RB3P	THIRD LVDS Receiver Signal (B+)	34	RCLK4P	FORTH LVDS Receiver Clock Signal(+)
14	RC3N	THIRD LVDS Receiver Signal (C-)	35	GND	Ground
15	RC3P	THIRD LVDS Receiver Signal (C+)	36	RD4N	FORTH LVDS Receiver Signal (D-)
16	GND	Ground	37	RD4P	FORTH LVDS Receiver Signal (D+)
17	RCLK3N	THIRD LVDS Receiver Clock Signal(-)	38	RE4N	FORTH LVDS Receiver Signal (E-)
18	RCLK3P	THIRD LVDS Receiver Clock Signal(+)	39	RE4P	FORTH LVDS Receiver Signal (E+)
19	GND	Ground	40	GND	Ground
20	RD3N	THIRD LVDS Receiver Signal (D-)	41	GND	Ground
21	RD3P	THIRD LVDS Receiver Signal (D+)	-		

Note: 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

2. LVDS pin (pin No. #22,23,38,39) are used for 10Bit(D) of the LCD module. If used for 8Bit(R), these pins are no connection.

Table 4-3. MODULE CONNECTOR(CN3) PIN CONFIGURATION

No	Symbol	Description
1	VSYNC	Vertical Sync signal
2	GND	Backlight Ground
3	GND	Backlight Ground
4	SIN	Local Dimming Serial Data
5	GND	Backlight Ground
6	SCLK	Local Dim Serial Clock
7	Reserved	No Connection
8	Reverse	Local Dimming data Reverse



3-2-2. Backlight Module

Master

- LED Driver Connector: 20022WR-14B1(Yeonho)

or Equivalent

- Mating Connector : 20022HS-14 or Equivalent

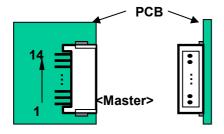
Table 5. LED DRIVER CONNECTOR PIN CONFIGURATION

Pin No	Symbol	Description	Note
1	VBL	Power Supply +24.0V	
2	VBL	Power Supply +24.0V	
3	VBL	Power Supply +24.0V	
4	VBL	Power Supply +24.0V	
5	VBL	Power Supply +24.0V	
6	GND	Backlight Ground	
7	GND	Backlight Ground	
8	GND	Backlight Ground	1
9	GND	Backlight Ground	
10	GND	Backlight Ground	
11	NC	No connection	
12	Von/off	Backlight ON/OFF control	
13	EXTVBR-B	External PWM	2
14	GND	Backlight Ground	3

Notes: 1. GND should be connected to the LCD module's metal frame.

- 2. High: on duty / Low: off duty, Pin#13 can be opened. (if Pin #13 is open, EXTVBR-B is 100%)
- 3. #14 of Input CNT Must be Connected to Backlight Ground.
- 4. Each impedance of pin #12 and 13 is over 50 [K Ω] and over 50 [K Ω].

♦ Rear view of LCM



3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6-1. TIMING TABLE for ATSC/NTSC (DE Only Mode)

ITE	М	Symbol	Min	Тур	Max	Unit	Note
	Display Period	thv	480	480	480	tCLK	1920 / 4
Horizontal	Blank	tнв	40	70	200	tCLK	1
	Total	tHP	520	550	680	tCLK	
	Display Period	tvv	1080	1080	1080	Lines	
Vertical	Blank	tvB	16	45	86	Lines	1
	Total	tvp	1096	1125	1166	Lines	
	DCLK	fclk	66.97	74.25	78.00	MHz	
Frequency	Horizontal	fH	121.8	135	140	KHz	2
	Vertical	fv	108	120	122	Hz	2

Table 6-2 TIMING TABLE for DVB/PAL (DE Only Mode)

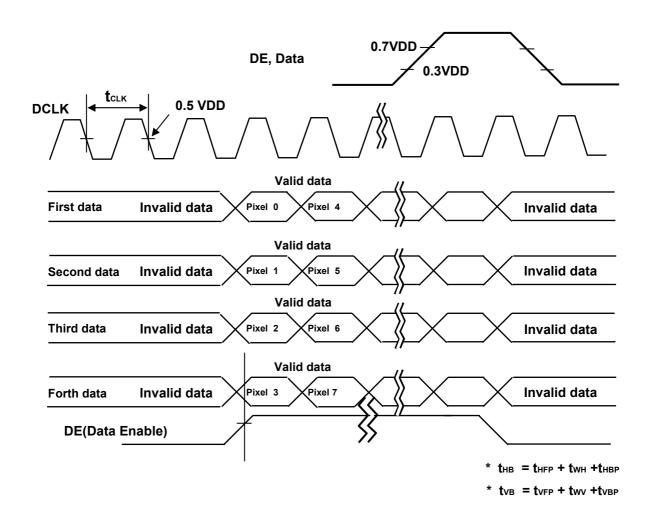
ITE	М	Symbol	Min	Тур	Max	Unit	Note
	Display Period	thv	480	480	480	tCLK	1920 / 4
Horizontal	Blank	tнв	40	70	200	tCLK	1
	Total	tHP	520	550	680	tCLK	
	Display Period	tvv	1080	1080	1080	Lines	
Vertical	Blank	tvв	228	270	300	Lines	1
	Total	tvp	1308	1350	1380	Lines	
	DCLK	fclk	66.97	74.25	78.00	MHz	
Frequency	Horizontal	fH	121.8	135	140	KHz	2
	Vertical	fv	95	100	104	Hz	2

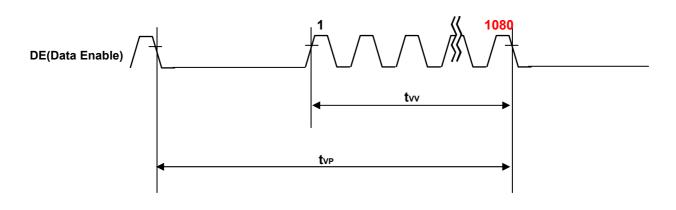
Note 1. The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). If you use spread spectrum for EMI, add some additional clock to minimum value for clock margin.

^{2.} The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency.

3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram

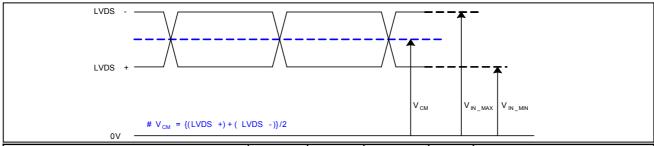




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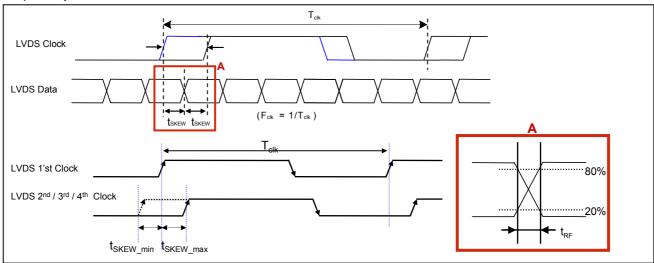
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	V _{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V _{IN}	0.7	1.8	V	-
Change in common mode Voltage	△VCM		250	mV	-

2) AC Specification

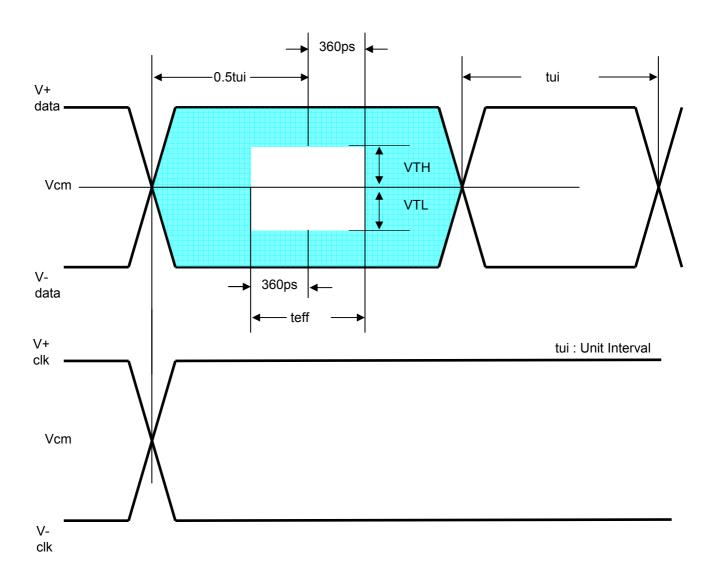


Description	Description				Unit	Note
LVDC Differential Voltage	High Threshold	V_{TH}	100	300	mV	2
LVDS Differential Voltage	Low Threshold	V_{TL}	-300	-100	mV	J
LVDS Clock to Data Skew Ma	t _{SKEW}		(0.25*T _{clk})/7	ps	-	
LVDS Clock/DATA Rising/Fall	LVDS Clock/DATA Rising/Falling time			(0.3*T _{clk})/7	ps	2
Effective time of LVDS	t _{eff}	±360		ps	-	
LVDS Clock to Clock Skew Ma	argin (Even to Odd)	t _{SKEW_EO}		1/7* T _{clk}	T _{clk}	-

Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range. 3. LVDS Differential Voltage is defined within t_{eff}

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3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

														In	put	Со	lor	Da	ta												
	Color	MS	ì.R			RE	ED				SB	M	S R		(3RI	EEI	N			SB	M	SB.			BL	UE				SB
				R7	R6	R5	R4	R3	R2	R1				G7	G6	G5	G4	G3	G2					B7	B6	B5	B4	B3	B2		
	Black			0	0	0	0	0	0	0	0		0	0	0	0	0	0	0		0		0	0	0	0	0	0	0	0	
	Red (1023)			1	1	1	1	1	1	_	1		0	0	0	0	0	0	0		0	- 	0	0	0	0	0	0		0	
	Green (1023)		0	0	0	0	0	0	0		0		1	1	1	1	1	1	1				0	0	0	0	0	0		0	
 	Blue (1023)	0		0	0	0	0	0	0	0			0	0	0		0	0	0		0	- .	1	1	1	1	1	1	1	1	
Basic Color	Cyan		0	0	0	0	0	0	0		0	1	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	_ 1	1	
	Magenta	-		_	1	_	_		_	1			0	0	0	0	0	0	0	0		'	1	1	1	1	1	1		1	
	Yellow		1	1	1	1	1	1	1	1		1	_	1	1	1	1	1	1		1	·	0	0	0	0	0	0		0	
	White		1	1	1	1	1	1	1	1				1	1	1	1	1	1	1	1	1	1	1	1	1	1		1	1	
				_	_	_	_		_				1	_		_	_							_		_	_	1	_		
	RED (0000)	<u> </u>	0		0	0	_			0			0	0	0	0	0	0	0	0		-	0	0	0	0	0	0		0	_
DED	RED (0001)	l ⁰	0	0	0	0	0		0	0	1	0	0	0	0		0			0	0	0	0			0		0		0	
RED		_	_	_	_			_	_	_																					
	RED (1022)		1	1	1	1	_	1	1	1			0	0	0	_	0	0	0	0		-	0	0	0	0	0	0		0	
	RED (1023)		1	1	1	1	_		_	1			0	0	0	0	0	0	0		0		0	0	0	0	0	0		0	_
	GREEN (0000)		0	0	0	0	0	0	0		0		0	0	0	_	0	_		0		-	0	0	0	0	0	0		0	_
	GREEN (0001)	0	0	0		0	0		0	0	0	0			0					0	1	0	0	0		0	0	0		0	0
GREEN								_	_	_		_	_					_	_					_		•		_	_		_
	GREEN (1022)				0		0			0			1	1		1	1	1		1			0		0	0		0	_	0	_
	GREEN (1023)	0	_	0	0	0	0		0	0	0	1	1		1	1	1	1	1	1	1		0	0	0	0	0	0	0 —	0	0
	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																						L							_		
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

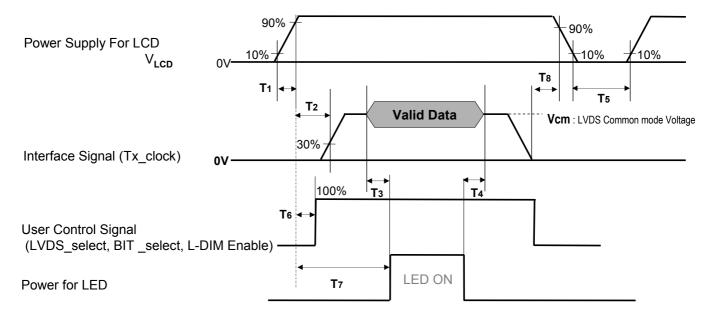


Table 8. POWER SEQUENCE

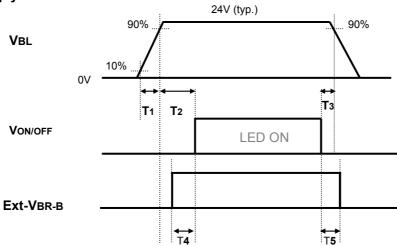
Donomotor		l lni4	Notes		
Parameter	Min	Unit	Notes		
T1	0.5	-	20	ms	
T2	0	-	-	ms	4
Т3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	s	5
T6	-	-	T2	ms	4
Т7	0.5		-	s	
Т8	100		-	ms	6

Note: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.
- 3. The T₃ / T₄ is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. If the on time of signals(Interface signal and user control signals) precedes the on time of Power(V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 5. T5 should be measured after the Module has been fully discharged between power off and on period.
- 6. It is recommendation specification that T8 has to be 100ms as a minimum value.

3-6-2. Sequence for LED Driver

Power Supply For LED Driver



3-6-3. Dip condition for LED Driver

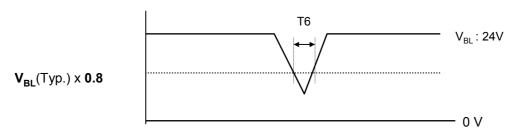


Table 9. Power Sequence for LED Driver

Devemates		Values		l laita	Damadra
Parameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
T3	10		-	ms	
T4	0	-	-	ms	
T5	0	-	-	ms	
T6	-	-	10	ms	V _{BL} (Typ) x 0.8

Notes: 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}C$. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

It is presented additional information concerning the measurement equipment and method in FIG. 1.

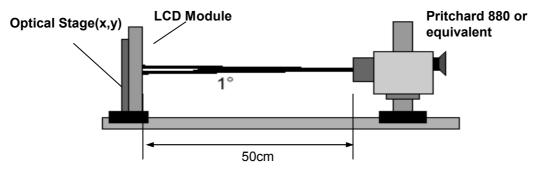


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= 25 \pm 2°C, V $_{LCD}$ =12.0V, fv=120Hz, Dclk=74.25MHz, EXTVBR-B =100%

D		O h			Value		1114	Mada
Par	ameter	Symb	OI	Min	Тур	Max	Unit	Note
Contrast Ratio		CR		1000	1400	-		1
Surface Lumina	nce, white	L_WH		360	450	-	cd/m ²	2
Luminance Vari	ation	δ_{WHITE}	5P	-	-	1.3		3
	Gray-to-Gray		G	-	5	8	ms	4,5
Response Time	MPRT	MPR	MPRT		8	12	ms	
Response Time	Uniformity	δ_{MPR}	Т	-	-	1		
	Uniformity	δ_{GTO}	G	-	-	1		
	RED	Rx			0.649			
	KED	Ry			0.332			
	GREEN	Gx			0.307			
Color Coordinat	es GREEN	Gy		Тур	0.595	Тур		
[CIE1931]	BLUE	Вх		-0.03	0.149	+0.03		
	BLUE	Ву			0.059			
	WHITE	Wx			0.279			
	VVIIIE	Wy			0.292			
Color Temperatu	ire				10,000		K	
Color Gamut					72		%	
Viewing Angle (CR>10)							
x a	axis, right(φ=0°)	θr		89	-	-		
x a	x axis, left (φ=180°)		θΙ		-	-	dograa	6
у а	y axis, up (φ=90°)		θu		-	-	degree	6
у а	axis, down (φ=270°)	θd		89	-	-		
Gray Scale				-	-	-		7

Notes: 1. Contrast Ratio(CR) is defined mathematically as:

CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)

CR = Surface Luminance at position n with all white pixels

Surface Luminance at position n with all black pixels

n =the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.

- 2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : $\delta \, \text{WHITE(5P)} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, \, L_{\text{on3}}, \, L_{\text{on4}}, \, L_{\text{on5}}) \, / \, \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, \, L_{\text{on3}}, \, L_{\text{on4}}, \, L_{\text{on5}}) \, / \, \text{Where } L_{\text{on1}} \, \text{to } L_{\text{on5}} \, \text{are the luminance with all pixels displaying white at 5 locations} \, .$ For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transit from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)

 * G to G Spec stands for average value of all measured points.

Photo Detector: RD-80S / Field: 2°

- 5. MPRT is defined as the 10% to 90% blur-edge width Bij(pixels) and scroll speed U(pixels/frame)at the moving picture. For more information, see FIG 4
- 6. Gray to Gray Response time uniformity is Reference data. Appendix IV-1.
- 7. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 5.
- 8. Gray scale specification
 Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
LO	0.07
L15	0.28
L31	1.05
L47	2.50
L63	4.69
L79	7.67
L95	11.47
L111	16.11
L127	21.64
L143	28.07
L159	35.43
L175	43.73
L191	52.99
L207	63.23
L223	74.47
L239	86.72
L255	100

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Measuring point for surface luminance & measuring point for luminance variation.

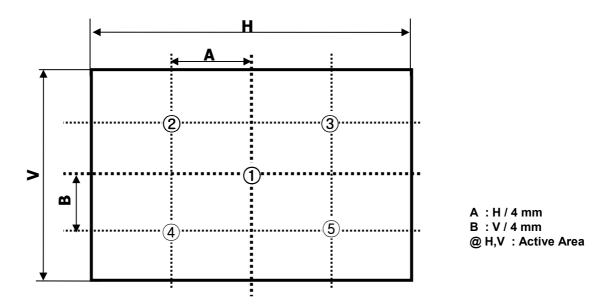


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

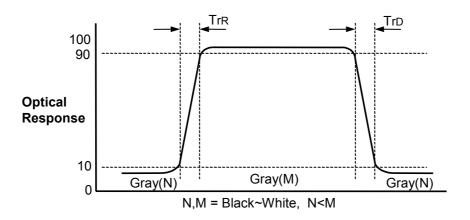


FIG. 3 Response Time

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MPRT is defined as the 10% to 90% blur-edge with Bij(pixels) and scroll speed U(pixels/frame)at the moving picture.

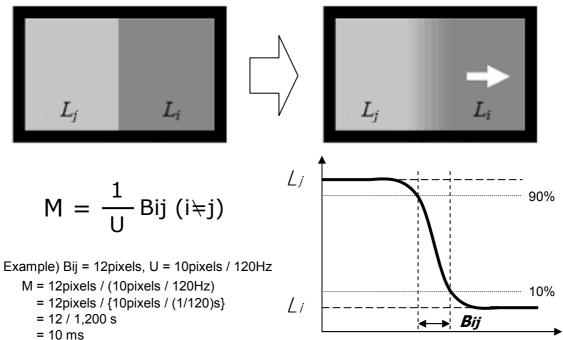


FIG. 4 MPRT

Dimension of viewing angle range

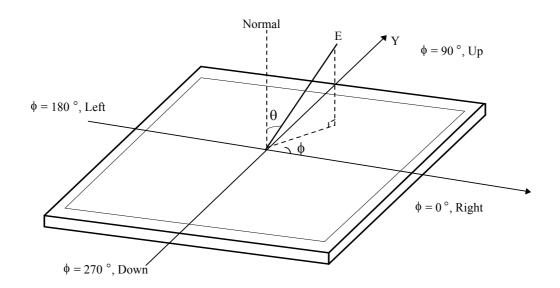


FIG. 5 Viewing Angle

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5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

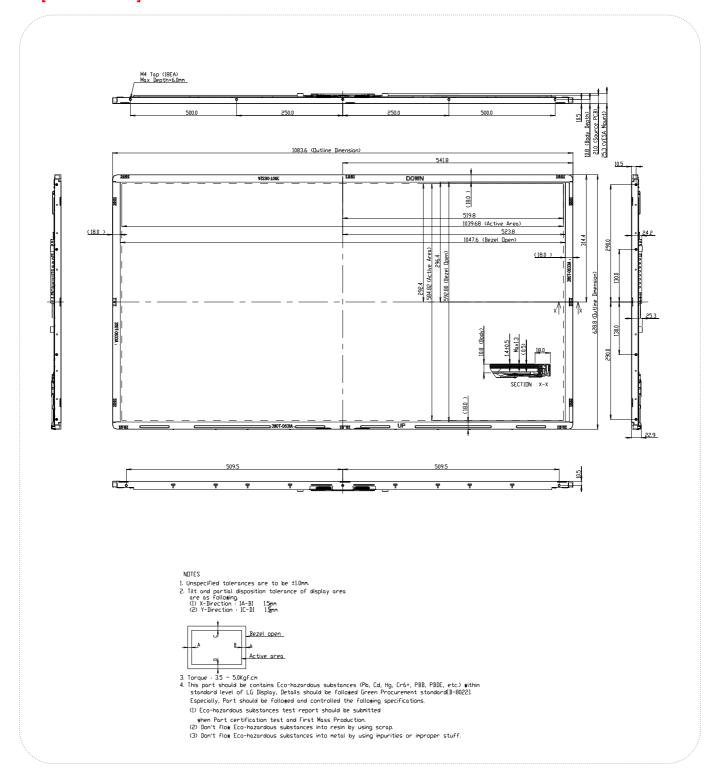
Table 12. MECHANICAL CHARACTERISTICS

Item		Value		
	Horizontal	1083.6 mm		
Outline Dimension	Vertical	628.8 mm		
	Depth	10.8 mm		
D I A	Horizontal	1047.6 mm		
Bezel Area	Vertical	592.8 mm		
Activo Display Area	Horizontal	1039.68 mm		
Active Display Area	Vertical	584.82 mm		
Weight	15.0 Kg (Typ.), 16.5kg (Max.)			

Note: Please refer to a mechanical drawing in terms of tolerance at the next page.

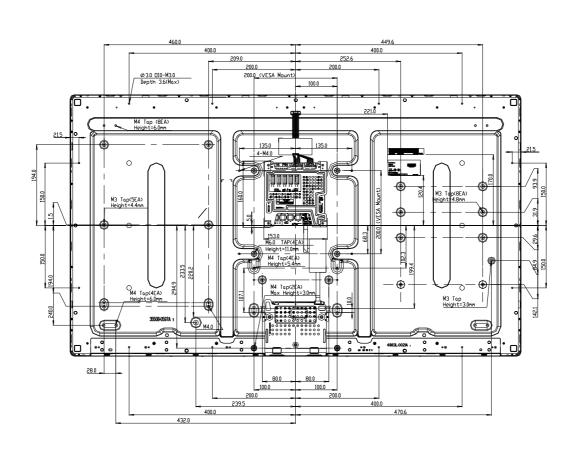
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[FRONT VIEW]



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[REAR VIEW]



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6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition					
1	High temperature storage test	rature storage test Ta= 60°C 240h					
2	Low temperature storage test	Ta= -20°C 240h					
3	High temperature operation test	Ta= 50°C 50%RH 240h					
4	Low temperature operation test	Ta= 0°C 240h					
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min Each direction per 10 min					
6	Shock test (non-operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : \pm X, \pm Y, \pm Z One time each direction					
7	Humidity condition Operation	Ta= 40 °C ,90%RH					
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft					

Note: Before and after Reliability test, LCM should be operated with normal function.

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7. International Standards

7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus Safety Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1: 2001 Embedded LED Power (Class1M) Power: 6.5896 mW (Max.) Wavelength: 277 ~526 (nm) Width: 1.6 x 0.6 (mm)

2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open. Do not open while operating.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

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8. Packing

8-1. Information of LCM Label

a) Lot Mark

Α	В	С	D	Е	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH)

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

	Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Ī	Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	Α	В	С

D:YEAR

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one Pallet: 13 pcs

b) Pallet Size: 1300 mm X 1140 mm X 850 mm

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9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

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9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

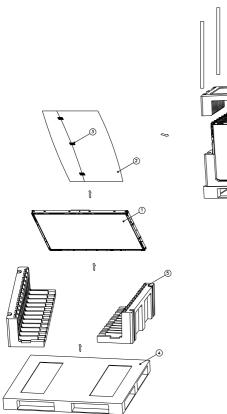
9-6. Handling Precautions for Protection Film

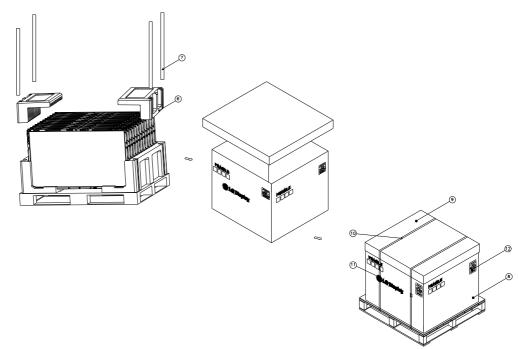
- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX-I

■ LC470EUD-SCA1 – Pallet Ass'y



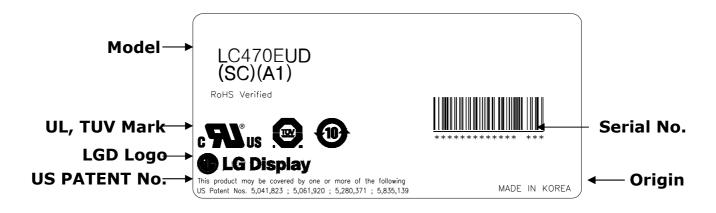


NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	47INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	PAPER 1300X1140X126MM
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE,POST	PAPER
8	ANGLE,PACKING	PAPER
9	BAND,CLIP	STEEL
10	BAND	PP
11	TAPE	OPP
12	LABEL	YUPO 80G 100X70

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APPENDIX- II-1

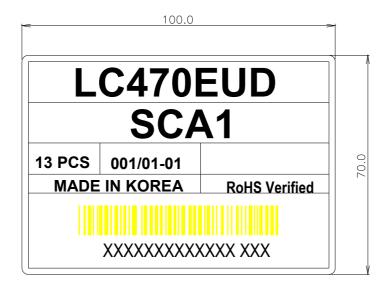
■ LC470EUD-SCA1-LCM Label



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APPENDIX- II-2

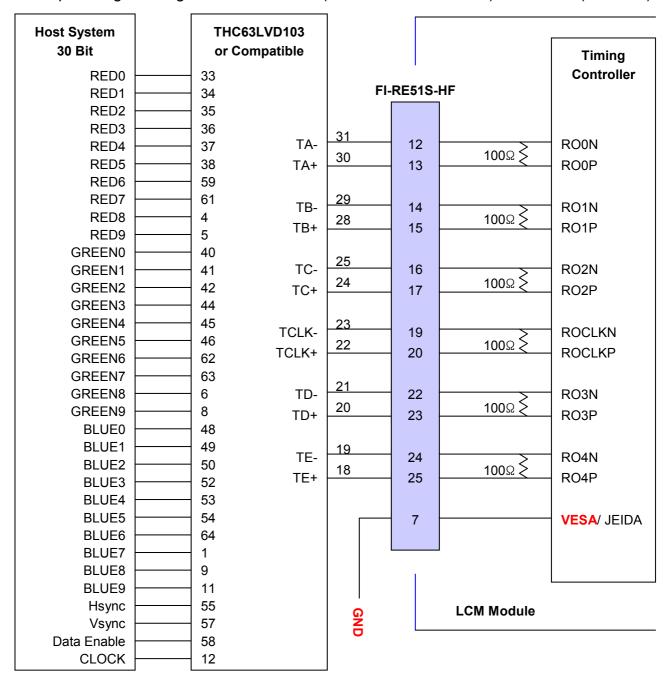
■ LC470EUD-SCA1-Pallet Label



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APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine: THC63LVD103) Transmitter(Pin7="L")



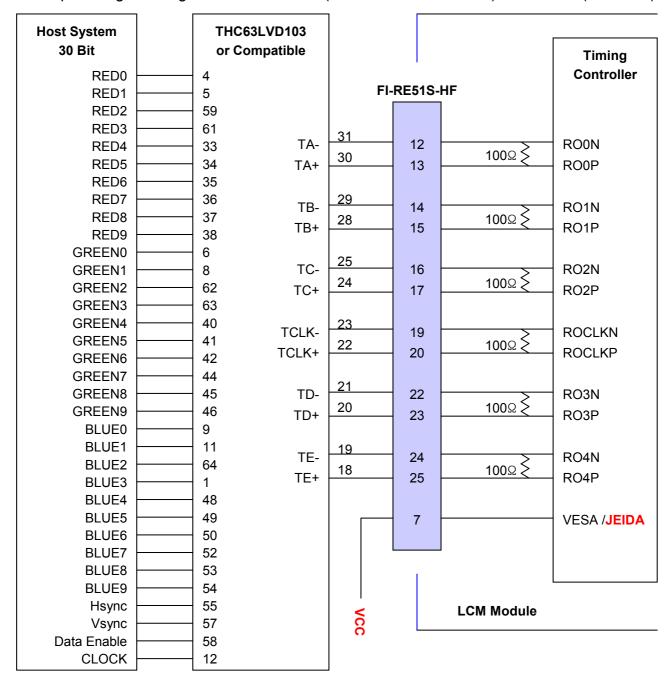
Note: 1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

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APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine: THC63LVD103) Transmitter(Pin7="H")



Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input

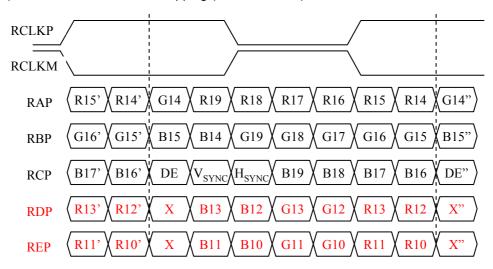
- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

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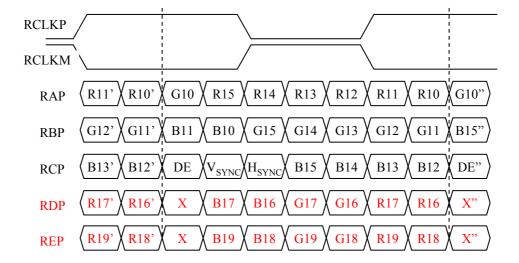
APPENDIX- III-2

■ LVDS Data-Mapping Information (10 Bit)

1) LVDS Select: "H" Data-Mapping (JEIDA format)



2) LVDS Select : "L" Data-Mapping (VESA format)

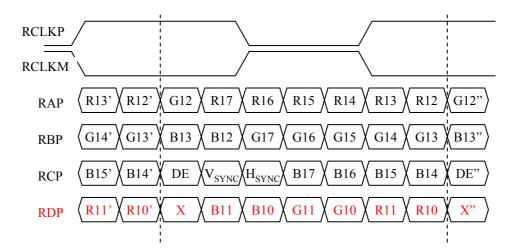


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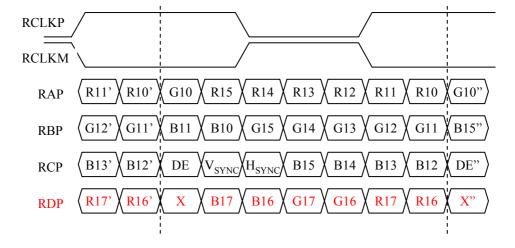
APPENDIX- III-2

■ LVDS Data-Mapping Information (8 Bit)

1) LVDS Select: "H" Data-Mapping (JEIDA format)



2) LVDS Select: "L" Data-Mapping (VESA format)

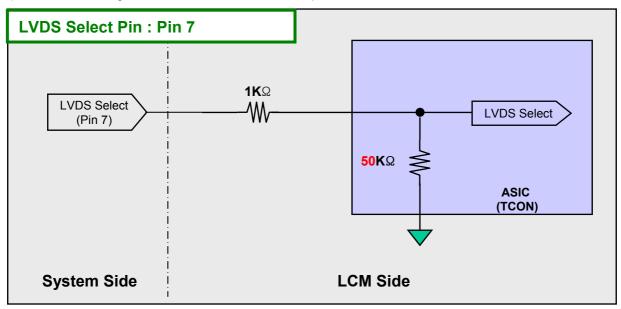


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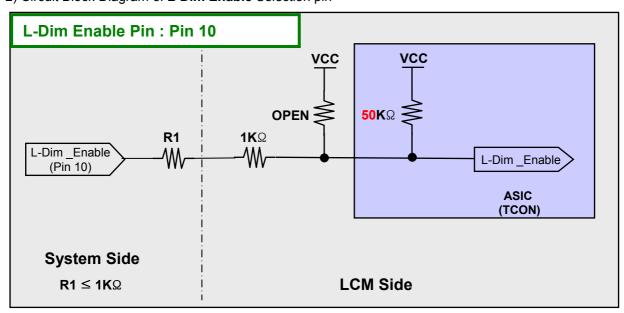
APPENDIX- III-3

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of L-Dim Enable Selection pin

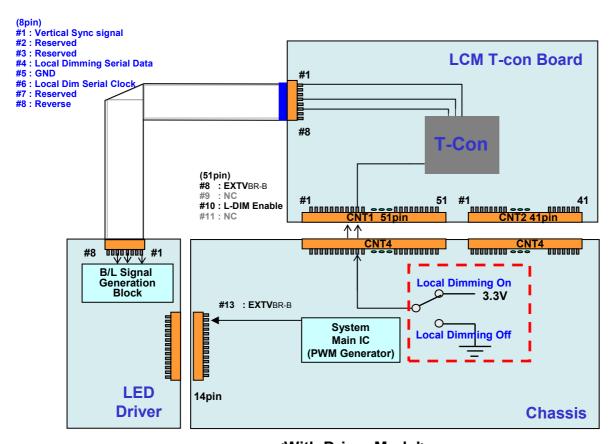


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APPENDIX- III-4

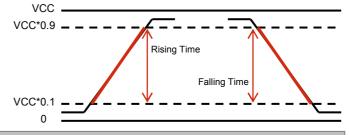
■ EXTVBR-B & Local Dimming Design Guide

- 1) When L-Dim Enable is "L", Vertical Sync Signal = System Dimming with 100Hz or 120Hz frequency.
- 2) Local Dimming signals are synchronized with V-Sync Freq. of System in T-Con Board.
- 3) EXTVBR-B Specification (VCC = 3.3V) @ Local Dimming
 - a) High Voltage Range : $2.5 \text{ V} \sim 3.6 \text{ V}$ b) Low Voltage Range : $0.0 \text{ V} \sim 0.8 \text{ V}$



<With Driver Model>

EXTV BR-B Frequency	MAX 1Khz Recommendation: 100 Hz for PAL 120 Hz for NTSC		
Rising Time	MAX 10.0 μs		
Falling Time	MAX 10.0 μs		



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APPENDIX- IV-1

Gray to Gray Response Time Uniformity

This is only the reference data of G to G and uniformity for LC470EUD-SCA1 model.

1. G to G Response Time:

Response time is defined as Figure 3 and shall be measured by switching the input signal for "Gray (N)" and "Gray(M)".(32Gray Step at 8bit)

2. G to G Uniformity

The variation of G to G Uniformity , δ $_{G}$ to $_{G}$ is defined as :

G to G Uniformity =
$$\frac{Maximum(GtoG) - Typical(GtoG)}{Typical(GtoG)} \le 1$$

*Maximum (GtoG) means maximum value of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).

	0Gray	127ray	255Gray		895Gray	1023Gray
00,500		·	<u> </u>		· · · · · ·	, ,
0Gray		TrR:0G→127G	TrR:0G→255G	•••	TrR:0G→895G	TrR:0G→1023G
127Gray	TrD:127G→0G		TrR:127G→255G	•••	TrR:127G→895G	TrR:127G→1023G
255Gray	TrD:255G→0G	TrD:255G→127G			TrR:255G→895G	TrR:255G→1023G
895Gray	TrD:895G→0G	TrD:895G→127G	TrD:895G→255G			TrR:895G→1023G
1023Gray	TrD:1023G→0G	TrD:1023G→127G	TrD:1023G→255G		TrD:1023G→895G	

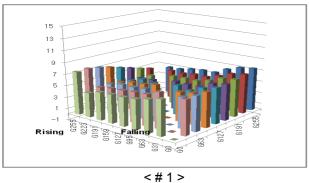
3. Sampling Size: 2 pcs

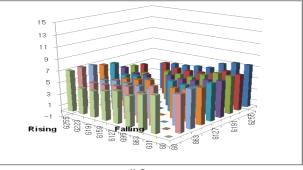
4. Measurement Method: Follow the same rule as optical characteristics measurement.

5. Current Status

Below table is actual data of production on Feb. 22. 2010 (LGD RV Event Sample)

	G to G Respo	Uniformity	
	Min.	Max.	Officiality
# 1	4.27	7.56	
# 2	4.10	7.45	





> <#2>

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