SPECIFICATION
FOR
APPROVAL

( ) Preliminary Specification
(●) Final Specification

Title	24.0" WUXGA TFT LCD

BUYER	NDS
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LM240WU7
SUFFIX	SLA1

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE / DATE
/	
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

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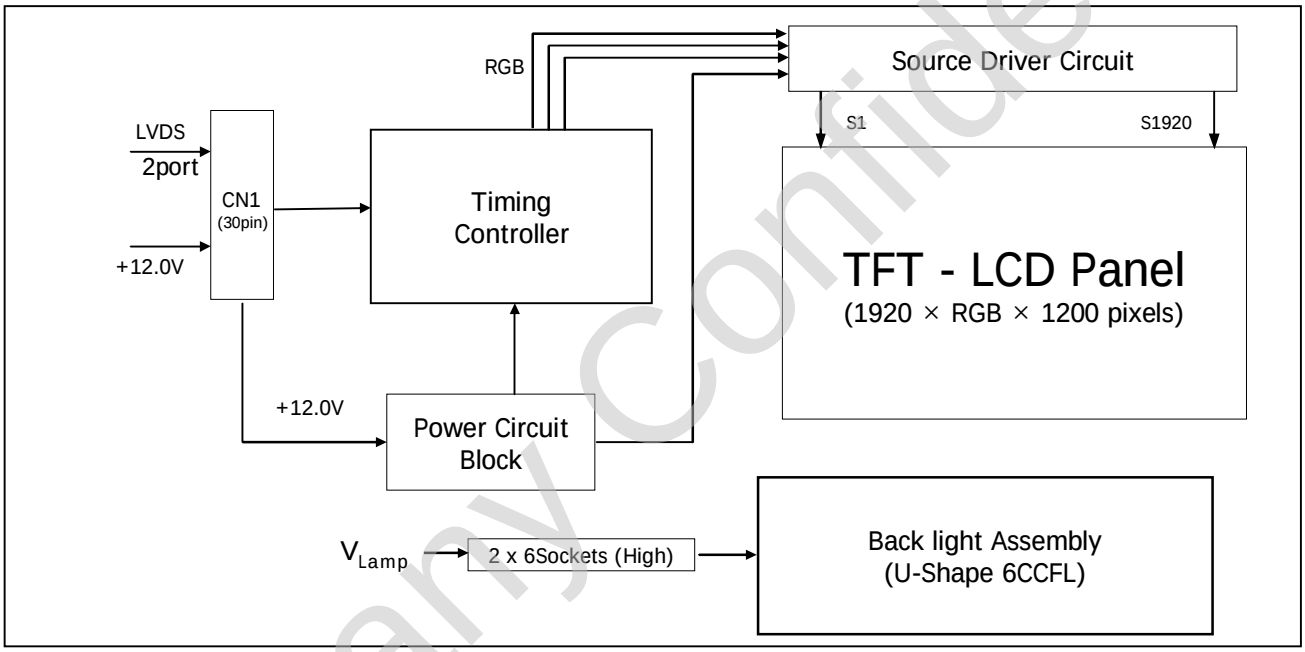
Contents

RECORD OF REVISIONS

Revision No	Revision Date	Page	Description
0.0	Nov. 7. 2008	-	First Draft(Preliminary)
0.1	Feb. 5. 2009	4	Update the General Features
		6	Update the Electircal characteristics
		19	Update the Optical specifications
		26, 27	Updated the Mechanical Drawing
0.2	Mar. 17. 2009	19	Update the Optical specifications
		26	Updated the Mechanical Drawing
1.0	Apr. 3. 2009	27	Updated the Mechanical Drawing

1. General Description

LM240WU7 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 24inch diagonally measured active display area with WUXGA resolution (1200 vertical by 1920 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M(True) colors.
It has been designed to apply the 8Bit 2 port LVDS interface.
It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



General Features

Active Screen Size	24.1 inches(61.13cm) diagonal
Outline Dimension	546.4(H) x 352.0(V) x 35.7(D) mm(Typ.) * without inverter
Pixel Pitch	0.270 mm x 0.270 mm
Pixel Format	1920 horiz. By 1200 vert. Pixels RGB stripes arrangement
Color Depth	8-bit, 16,777,216 colors
Luminance, White	400 cd/m^2 (Center 1 points)
Viewing Angle(CR>10)	View Angle Free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 69.72 Watt (Typ.) (6.72 Watt@VLCD, 63 Watt @Vlamp)
Weight	2830 g (typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer

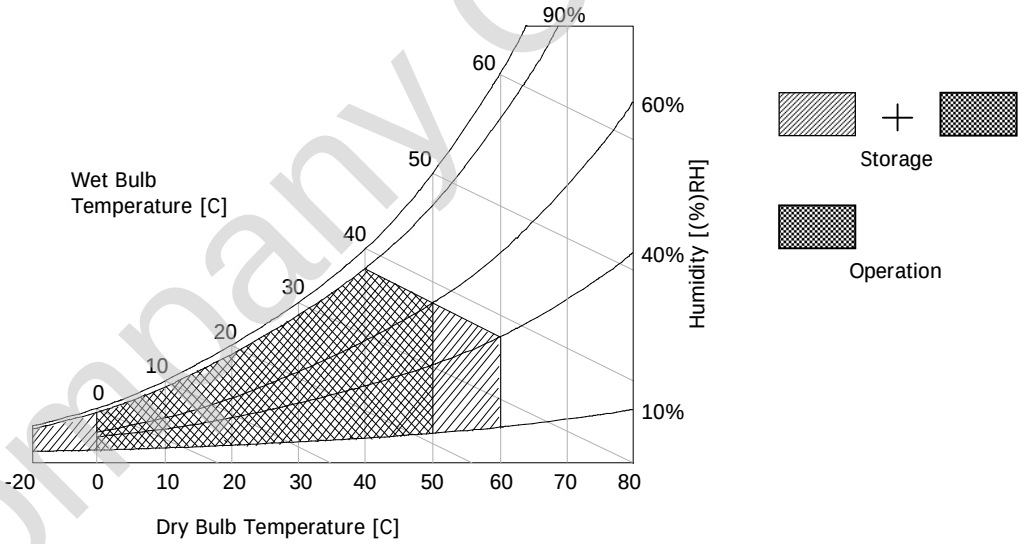
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	VLCD	-0.3	14	Vdc	at 25 ± 2°C
Operating Temperature	TOP	0	50	°C	1, 2
Storage Temperature	TST	-20	60	°C	
Operating Ambient Humidity	HOP	10	90	%RH	
Storage Humidity	HST	10	90	%RH	

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C Max, and no condensation of water.
Note : 2. Maximum Storage Humidity is up to 40℃, 70% RH only for 4 corner light leakage Mura.



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCDs.

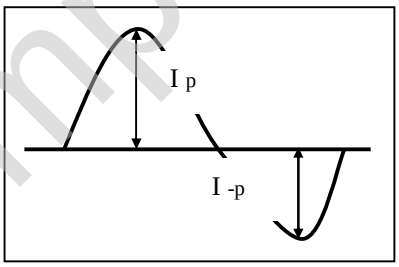
Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	V_{LCD}	11.4	12.0	12.6	Vdc	
Permissive Power Input Ripple	V_{RF}			400	mV	1
Power Supply Input Current	I_{LCD}	476	560	644	mA	2
		625	735	845	mA	3
Differential Impedance	Zm	90	100	110	ohm	
Power Consumption	P_{LCD}	-	6.72	7.73	Watt	2
Rush current	I_{RUSH}	-	-	3	A	4
LAMP :						
Operating Voltage	V_{BL}	1550(8.0mA)	1750(6.0mA)	1950(3.0mA)	V_{RMS}	5, 6
Operating Current	I_{BL}	3.0	6.0	8.0	mA_{RMS}	5
Established Starting Voltage	Vs					5,7
at 25 °C				2500	V_{RMS}	
at 0 °C				2900	V_{RMS}	
Operating Frequency	f_{BL}	40	55	80	kHz	8
Discharge Stabilization Time	Ts		-	3	Min	5, 9
Power Consumption	P_{BL}		63	69.3	Watt	10
Life Time		50,000			Hrs	5, 11

Note : The design of the inverter must have specifications for the lamp in LCD Assembly.
The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.
When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in you instrument.

Note : Do not Insert conducting Meterial to lamp connecting socket. If the conducting Meterial is inserted to the lamp connecting sockets, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp connecting socket and conducting material.

1. Permissive power ripple should be measured under V_{LCD}=12.0V, 25 ± 2°C,f_V=60Hz condition and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20Mhz.
2. The specified current and power consumption are under the V_{LCD}=12.0V, 25 ± 2°C,f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
3. The current is specified at the maximum current pattern.
4. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).
5. Specified values are for a single lamp.
6. Operating voltage is measured at 25 ± 2°C. The variance of the voltage is ± 10%.
7. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. (Inverter open voltage must be more than lamp starting voltage.) Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
8. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
9. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%. T_S is the time required for the brightness of the center of the lamp to be not less than 95%. The used lamp current is the lamp typical current.
10. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current. (P_{BL} = V_{BL} x I_{BL} x N_{Lamp})
11. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2°C.
12. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following. It shall help increase the lamp lifetime and reduce leakage current.
    a. The asymmetry rate of the inverter waveform should be less than 10%.
    b. The distortion rate of the waveform should be within √2 ±10%.
    * Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

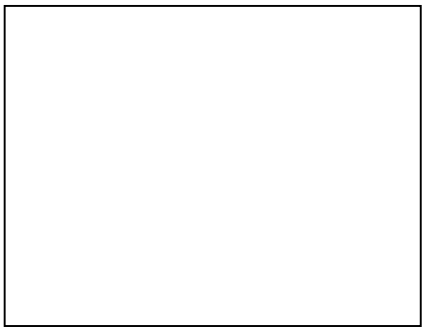
$| I_p - I_{-p} | / I_{rms}$ x 100%

* Distortion rate

I_p (or I_{-p}) / I_{rms}

13. The inverter which is combined with this LCM, is highly recommended to connect coupling(ballast) condenser at the high voltage output side. When you use the inverter which has not coupling(ballast) condenser, it may cause abnormal lamp lighting because of biased mercury as time goes.
14. In case of edgy type back light with over 6 parallel lamps, input current and voltage wave form should be synchronized

< Permissive Power Input Ripple (V_{LCD}=12.0V, 25 ± 2°C,f_V=60Hz) **>**



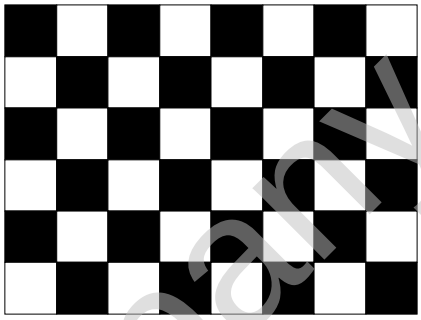
White Pattern

Black Pattern

< Power consumption (V_{LCD}=12.0V, 25 ± 2°C,f_V=60Hz) **>**

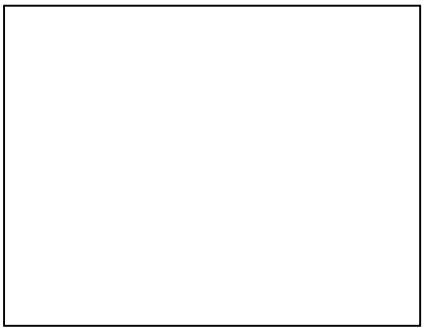
Typical current pattern

(White : 255Gray, Black : 0Gray)



Mosaic Pattern(8 x 6)

Maximum current pattern



White Pattern

[Figure 1] Mosaic pattern & Black Pattern for power consumption measurement

3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(CN1) : IS100-L30B-C23 (UJU), KDF71G-30S-1H (Hirose) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 3 MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	FR0M	Minus signal of odd channel 0 (LVDS)	16	SR1P	Plus signal of even channel 1 (LVDS)
2	FR0P	Plus signal of odd channel 0 (LVDS)	17	GND	Ground
3	FR1M	Minus signal of odd channel 1 (LVDS)	18	SR2M	Minus signal of even channel 2 (LVDS)
4	FR1P	Plus signal of odd channel 1 (LVDS)	19	SR2P	Plus signal of even channel 2 (LVDS)
5	FR2M	Minus signal of odd channel 2 (LVDS)	20	SCLKINM	Minus signal of even clock channel (LVDS)
6	FR2P	Plus signal of odd channel 2 (LVDS)	21	SCLKINP	Plus signal of even clock channel (LVDS)
7	GND	Ground	22	SR3M	Minus signal of even channel 3 (LVDS)
8	FCLKINM	Minus signal of odd clock channel (LVDS)	23	SR3P	Plus signal of even channel 3 (LVDS)
9	FCLKINP	Plus signal of odd clock channel (LVDS)	24	GND	Ground
10	FR3M	Minus signal of odd channel 3 (LVDS)	25	OPEN	NC
11	FR3P	Plus signal of odd channel 3 (LVDS)	26	OPEN	NC
12	SR0M	Minus signal of even channel 0 (LVDS)	27	PWM	PWM_OUT for Wavy Noise
13	SR0P	Plus signal of even channel 0 (LVDS)	28	ODC ON	ODC ON/OFF Control H : ODC ON , L : ODC OFF Default : ODC ON (No connection)
14	GND	Ground	29	VLCD	Power Supply +12.0V
15	SR1M	Minus signal of even channel 1 (LVDS)	30	VLCD	Power Supply +12.0V

Note: 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
2. All V_{LCD} (power input) pins should be connected together.
3. Input Level of LVDS signal is based on the EIA 664 Standard.

User Connector Diagram

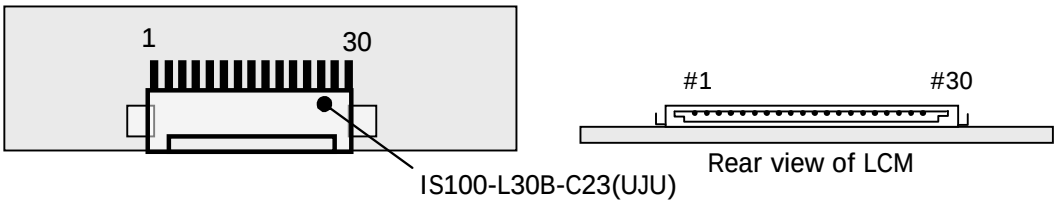


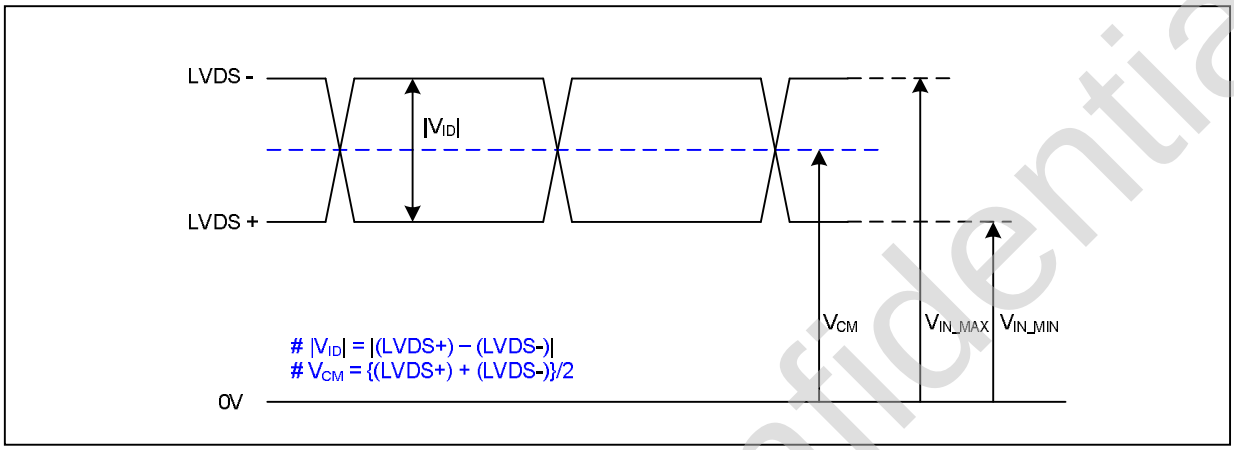
Table 4. REQUIRED SIGNAL ASSIGNMENT FOR Flat Link (TI:SN75LVDS83) Transmitter

Pin #	Pin Name	Require Signal	Pin #	Pin Name	Require Signal
1	Vcc	Power Supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input (R7)	30	D26	TTL Input (DE)
3	D6	TTL Input (R5)	31	T_X CLKIN	TTL Level clock Input
4	D7	TTL Input (G0)	32	PWR DWN	Power Down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input (G1)	34	PLL Vcc	Power Supply for PLL
7	D9	TTL Input (G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input (G6)	36	LVDS GND	Ground pin for LVDS
9	Vcc	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output 3
10	D11	TTL Input (G7)	38	TxOUT3-	Negative LVDS differential data output 3
11	D12	TTL Input (G3)	39	T_X CLKOUT+	Positive LVDS differential clock output
12	D13	TTL Input (G4)	40	T_X CLKOUT-	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	T_X OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input (G5)	42	T_X OUT2-	Negative LVDS differential data output 2
15	D15	TTL Input (B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input (B6)	44	LVDS Vcc	Power Supply for LVDS
17	Vcc	Power Supply for TTL Input	45	T_X OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input (B7)	46	T_X OUT1-	Negative LVDS differential data output 1
19	D18	TTL Input (B1)	47	T_X OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input (B2)	48	T_X OUT0-	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input (B3)	50	D27	TTL Input (R6)
23	D21	TTL Input (B4)	51	D0	TTL Input (R0)
24	D22	TTL Input (B5)	52	D1	TTL Input (R1)
25	D23	TTL Input (RSVD)	53	GND	Ground pin for TTL
26	Vcc	Power Supply for TTL Input	54	D2	TTL Input (R2)
27	D24	TTL Input (HSYNC)	55	D3	TTL Input (R3)
28	D25	TTL Input (VSYNC)	56	D4	TTL Input (R4)

Notes : Refer to LVDS Transmitter Data Sheet for detail descriptions.

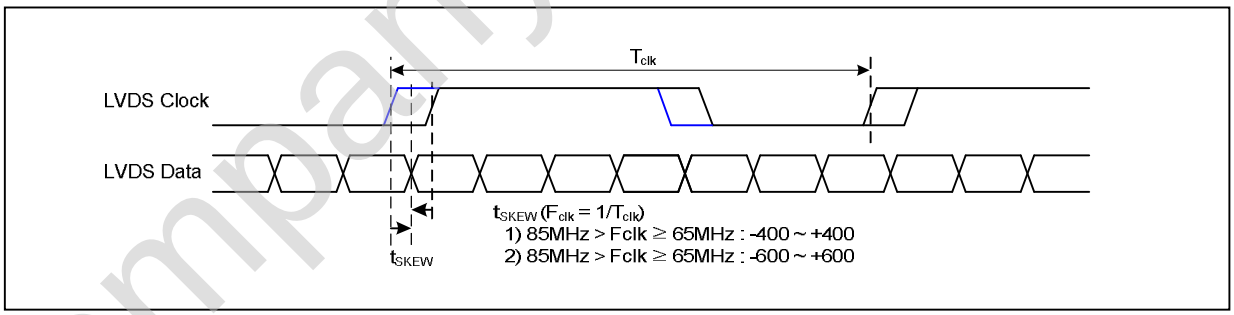
LVDS Input characteristics

1. DC Specification

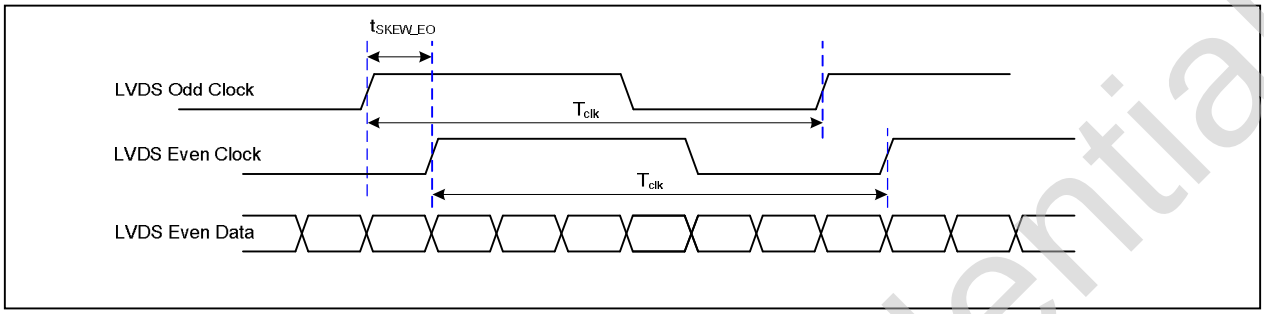


Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	$\|V_{ID}\|$	100	600	mV	-
LVDS Common mode Voltage	V_{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V_{IN}	0.3	2.1	V	-

2. AC Specification



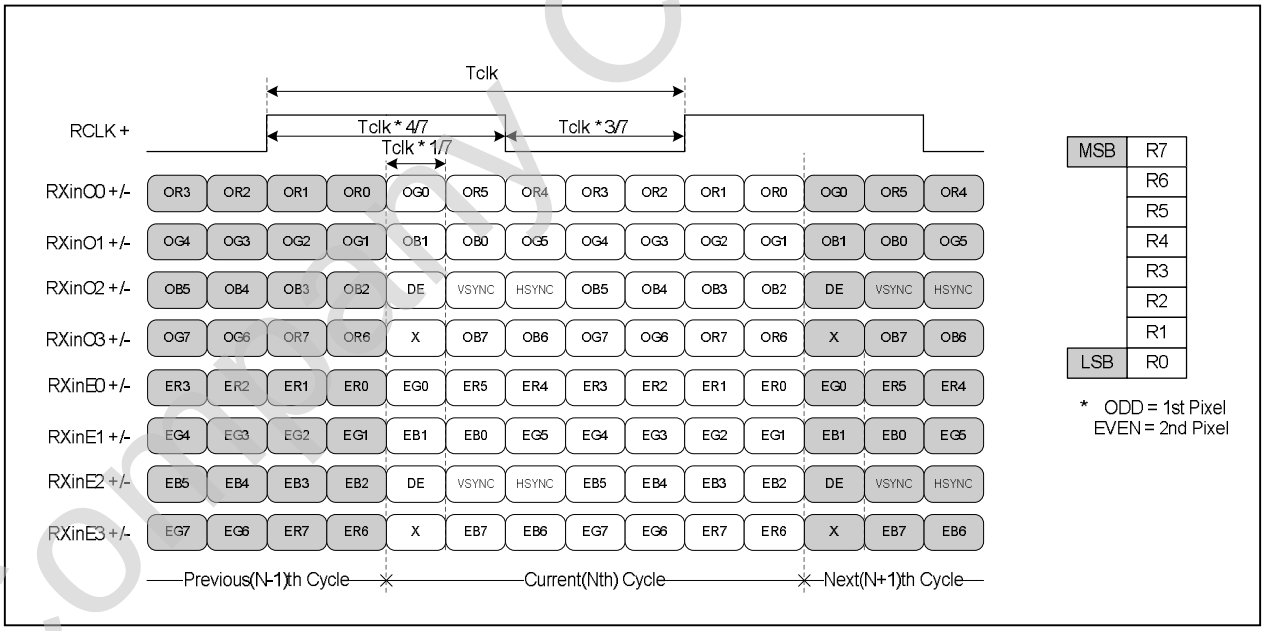
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skew Margin	t_{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
	t_{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t_{SKEW_EO}	- 1/7	+ 1/7	T_{clk}	-



< Clock skew margin between channel >

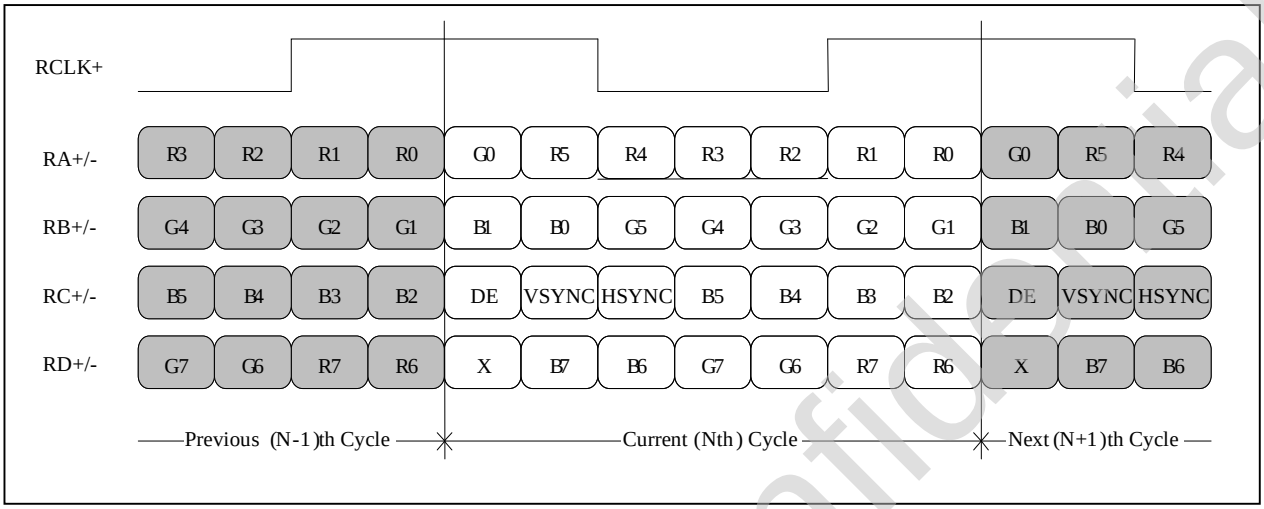
3. Data Format

1) LVDS 2 Port



< LVDS Data Format >

2) LVDS 1 Port



3-3. Signal Timing Specifications

This is signal timing required at the input of the TMDS transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

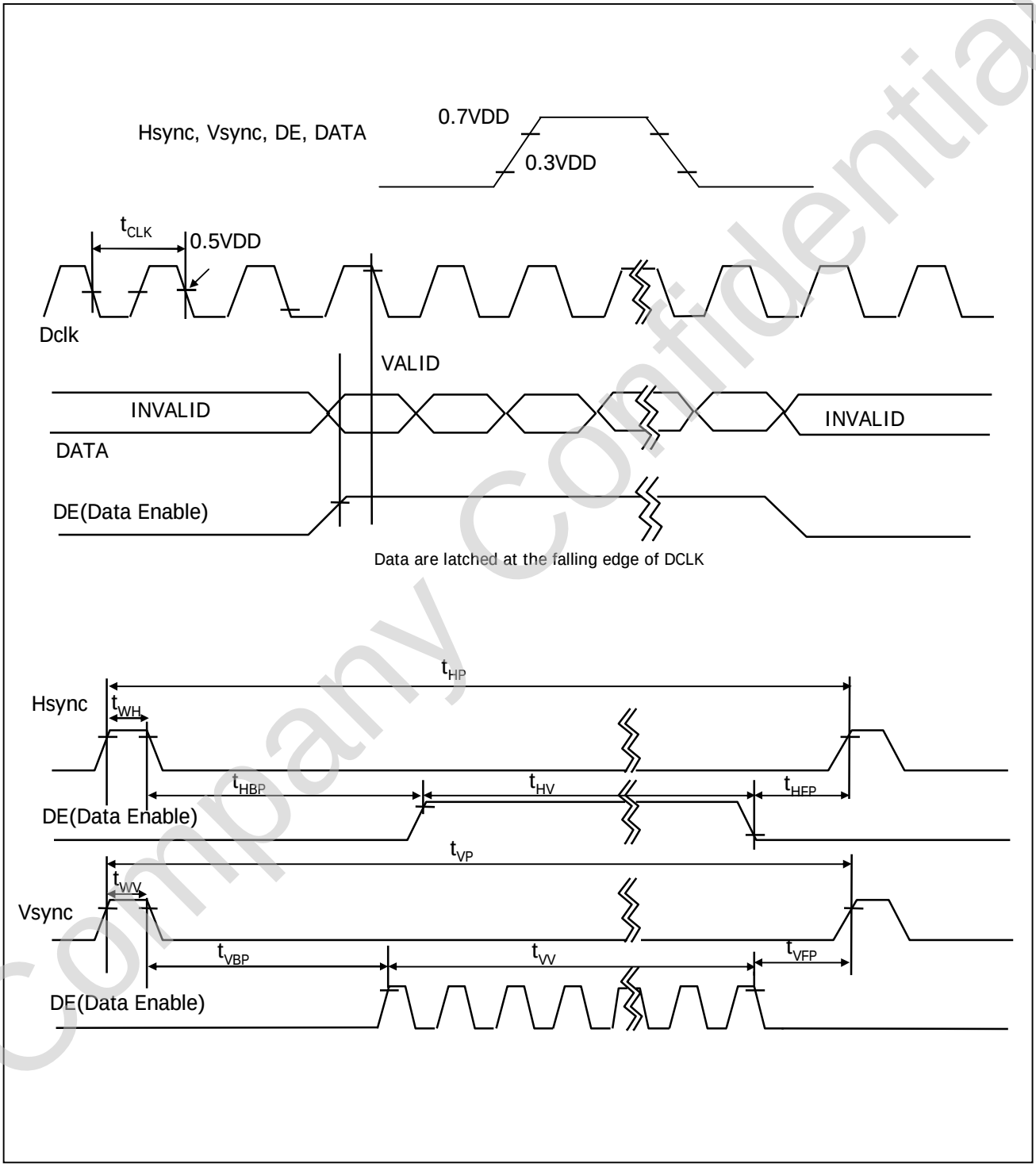
Table 5. TIMING TABLE (VESA COORDINATED VIDEO TIMING)

ITEM		SYMBOL	Min	Typ	Max	Unit	Note
DCLK	Period	t_{CLK}	12.82	12.98	13.16	ns	Pixel frequency : Typ. 154MHz
	Frequency	f_{CLK}	76	77	78	MHz	
Hsync	Period	t_{HP}	1036	1040	1044	t_{CLK}	
	Width-Active	t_{WH}	16	16	16		
Vsync	Period	t_{VP}	1233	1235	1237	t_{HP}	
	Frequency	f_V	58.85	59.95	61	Hz	
	Width-Active	t_{WV}	6	6	6	t_{HP}	
Data Enable	Horizontal Valid	t_{HV}	960	960	960	t_{CLK}	
	Horizontal Back Porch	t_{HBP}	36	40	44		
	Horizontal Front Porch	t_{HFP}	20	24	28		
	Horizontal Blank	-	76	80	84		$t_{WH}+ t_{HBP}+ t_{HFP}$
	Vertical Valid	t_{VV}	1200	1200	1200	t_{HP}	
	Vertical Back Porch	t_{VBP}	25	26	27		
	Vertical Front Porch	t_{VFP}	2	3	4		
	Vertical Blank	-	33	35	37		$t_{WV}+ t_{VBP}+ t_{VFP}$

Note: Hsync period and Hsync width-active should be even number times of t_{CLK}. If the value is odd number times of t_{CLK}, display control signal can be asynchronous. In order to operate this LCM a Hsync, Vsyn, and DE(data enable) signals should be used.

1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
2. Vsync and Hsync should be keep the above specification.
3. Hsync Period, Hsync Width, and Horizontal Back Porch should be any times of of character number(8).
4. The polarity of Hsync, Vsync is not restricted.

3-4. Signal Timing Waveforms



3-5. Color Input Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 6. COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...				...								...								...				
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...				...								...								...				
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	...				...								...								...				
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

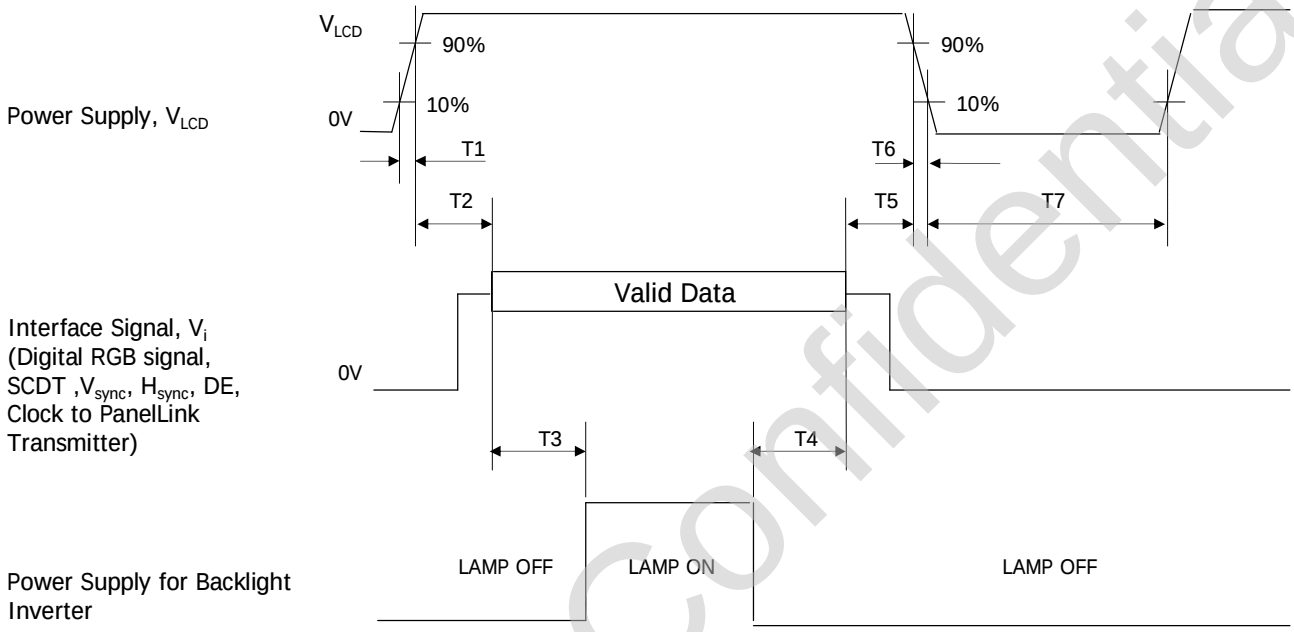
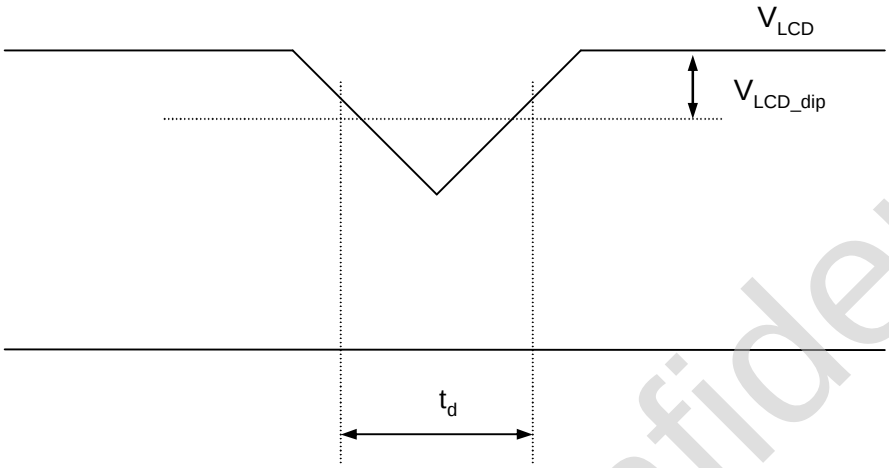


Table 7. POWER SEQUENCE

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0.01	-	50	ms
T3	500	-	-	ms
T4	200	-	-	ms
T5	0.01	-	50	ms
T7	500		-	ms

Notes : 1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

3-7. V_{LCD} Power Dip Condition



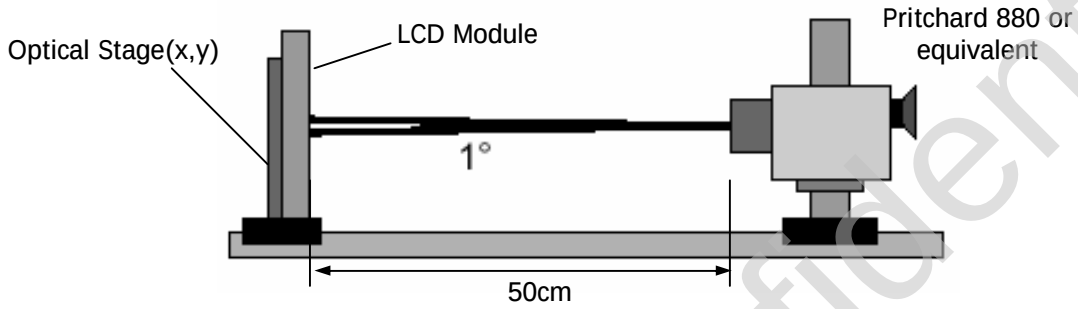
[Figure 2] Power dip condition

Dip condition

V_{LCD}_dip $\leq V_{LCD\_typ}$ X 0.2, $t_d \leq$ 20ms

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at 25±2°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 ° and aperture 1 degree.
FIG 3 presents additional information concerning the measurement equipment and method.



[FIG 3] Optical Characteristic Measurement Equipment and Method

Table 8. OPTICAL CHARACTERISTICS (Ta=25 °C, V_{LCD}=12.0V, f_V=60Hz Dclk=154MHz, V_{BR}=3.3V)

Parameter		Symbol	Values			Units	Notes
			Min	Typ	Max		
Contrast Ratio		CR	700	1000			1
Surface Luminance, white		L_{WH}	320	400		cd/m²	2
Luminance Variation		δ_{WHITE}	75			%	3
Response Time	Rise Time	Tr_R	-	7.5	12	ms	4
	Decay Time	Tr_D	-	7.5	12	ms	4
	Gray to Gray	T_{GTG_AVR}	-	5	-	ms	5
		T_{GTG_MAX}	-	-	12	ms	5
Color Coordinates [CIE1931]	RED	Rx	Typ -0.03	0.644	Typ +0.03		
		Ry		0.335			
	GREEN	Gx		0.304			
		Gy		0.613			
	BLUE	Bx		0.146			
		By		0.070			
	WHITE	Wx		0.313			
		Wy		0.329			
Color Shift	Horizontal	θ_{CST_H}	-	178	-	Degree	6
	Vertical	θ_{CST_V}	-	178	-		
Viewing Angle (CR>10)							
General	Horizontal	θ_H	170	178	-	Degree	7
	Vertical	θ_V	170	178	-		
Effective	Horizontal	θ_{GMA_H}		178	-	Degree	8
	Vertical	θ_{GMA_V}		178	-		
Gray Scale				2.2			9

Notes 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center point(Location P1)

2. Surface luminance(L_{WH})is luminance value at 5 points average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 4.
L_{WH} = = Average[$L_{on}1, L_{on}2, L_{on}3, L_{on}4, L_{on}5$]

3. The variation in surface luminance , δ WHITE is defined as :

$$d_{WHITE} = \frac{\text{Minimum}(L_{P1}, L_{P2}, \ldots . L_{P9})}{\text{Maximum}\ (L_{P1}, L_{P2}, \ldots . L_{P9})} \times 100$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations.
For more information see FIG 4.

4. Response time is the time required for the display to transition from black to white (Rise Time, Tr_R) and from white to black (Decay Time, Tr_D). For additional information see FIG 5.

5. Gray to gray response time is the time required for the display to transition from gray to gray. For additional information see Table 9.

6. Color shift is the angle at which the color difference is lower than 0.04.
For more information see FIG 6.
- Color difference (Δu′v′)

$$u' = \frac{4x}{-2x+12y+3} \qquad v' = \frac{9y}{-2x+12y+3}$$

$$\Delta u'v' = \sqrt{(u'_1 - u'_2)^2 + (v'_1 - v'_2)^2}$$

u′1, v′1 : u′v′ value at viewing angle direction
u′2, v′2 : u′v′ value at front (Θ=0)

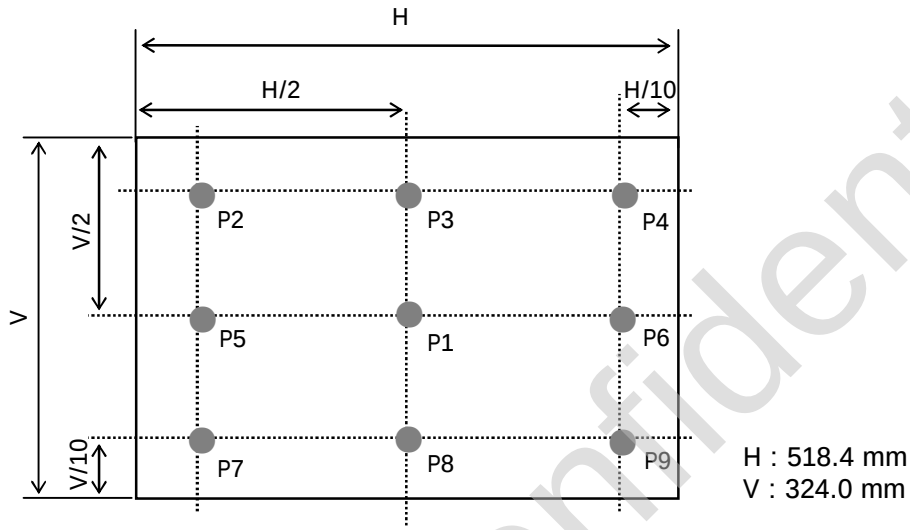
- Pattern size : 25% Box size
- Viewing angle direction of color shift : Horizontal, Vertical

7. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 7.

8. Effective viewing angle is the angle at which the gamma shift of gray scale is lower than 0.3.
For more information see FIG 8 and FIG 9.

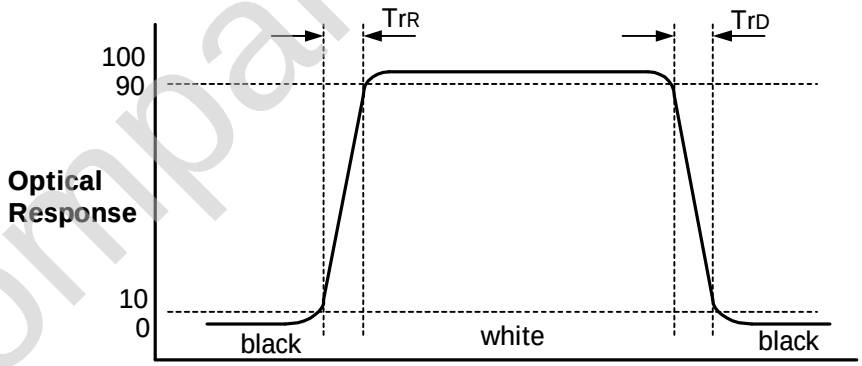
9. Gray scale specification
Gamma Value is approximately 2.2. For more information see Table 10.

Measuring point for surface luminance & measuring point for luminance variation.



[FIG 4] Measure Point for Luminance

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[FIG 5] Response Time

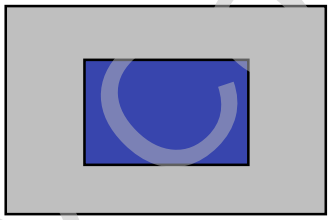
The gray to gray response time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray".

- Gray step : 5 step
- T_{GTG_AVR} is the total average time at rising time and falling time for "Gray To Gray".
- T_{GTG_MAX} is the max time at rising time or falling time for "Gray To Gray".

Table 9. Gray to gray response time table

Gray to Gray		Rising Time				
		G255	G191	G127	G63	G0
Falling Time	G255					
	G191					
	G127					
	G63					
	G0					

Color shift is defined as the following test pattern and color.



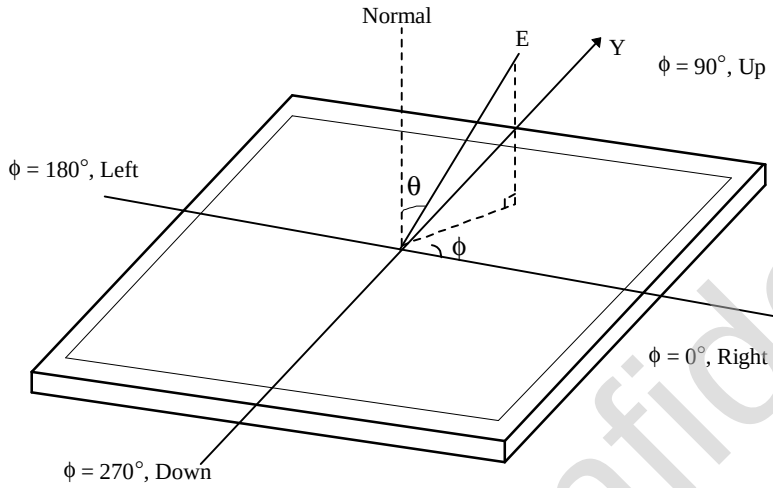
25% Box size

[FIG 6] Color Shift Test Pattern

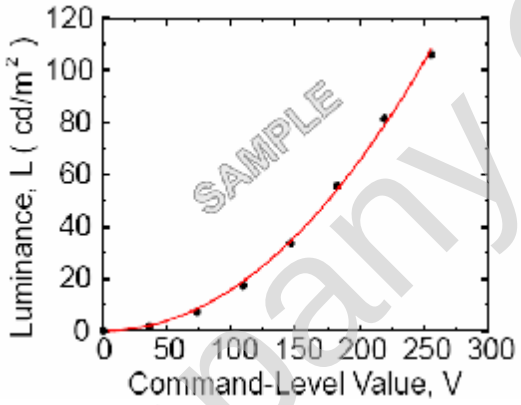
Average RGB values in Bruce RGB for Macbeth Chart

	Dark skin	Light skin	Blue sky	Foliage	Blue flower	Bluish green
R	98	206	85	77	129	114
G	56	142	112	102	118	199
B	45	123	161	46	185	178
	Orange	Purplish blue	Moderate red	Purple	Yellow green	Orange yellow
R	219	56	211	76	160	230
G	104	69	67	39	193	162
B	24	174	87	86	58	29
	Blue	Green	Red	Yellow	Magenta	Cyan
R	26	72	197	241	207	35
G	32	148	27	212	62	126
B	145	65	37	36	151	172
	White	Neutral 8	Neutral 6.5	Neutral 5	Neutral 3.5	Black
R	240	206	155	110	63	22
G	240	206	155	110	63	22
B	240	206	155	110	63	22

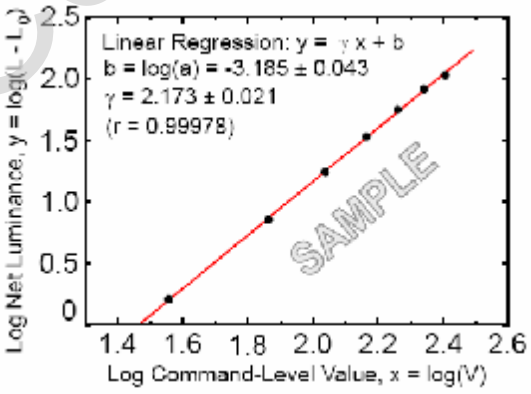
Dimension of viewing angle range.



[FIG 7] Viewing angle



[FIG 8] Sample Luminance vs. gray scale (using a 256 bit gray scale)



[FIG 9] Sample Log-log plot of luminance vs. gray scale

$$L = aV^{r} + L_b$$

$$\log(L - L_b) = r\log(V) + \log(a)$$

Here the Parameter α and γ relate the signal level V to the luminance L.

The GAMMA we calculate from the log-log representation (FIG. 9)

Table 10. Gray Scale Specification

Gray Level	Relative Luminance [%] (Typ.)
0	0.13
31	1.2
63	4.7
95	11.7
127	21.2
159	35.2
191	53.0
223	75.4
255	100

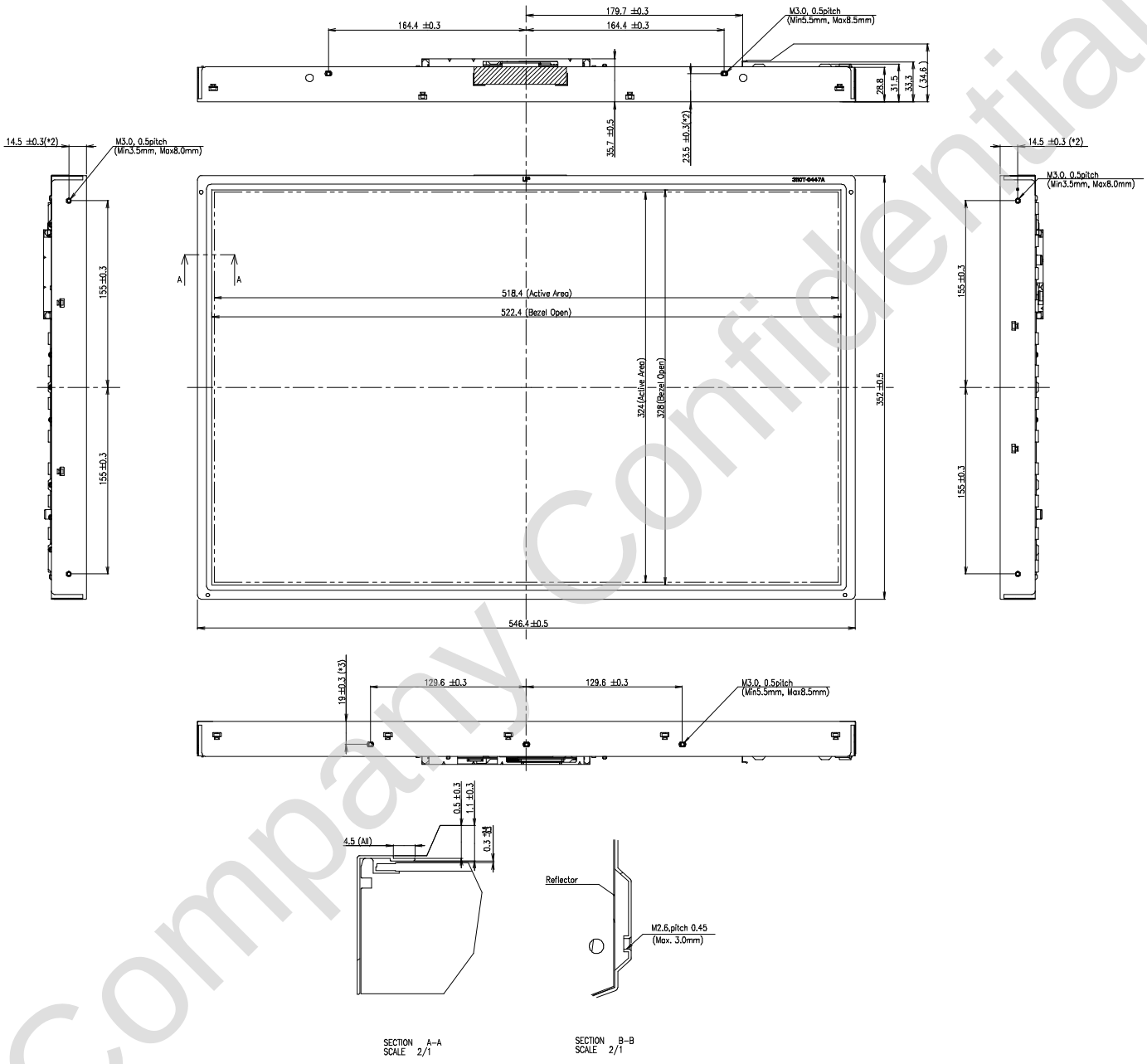
5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

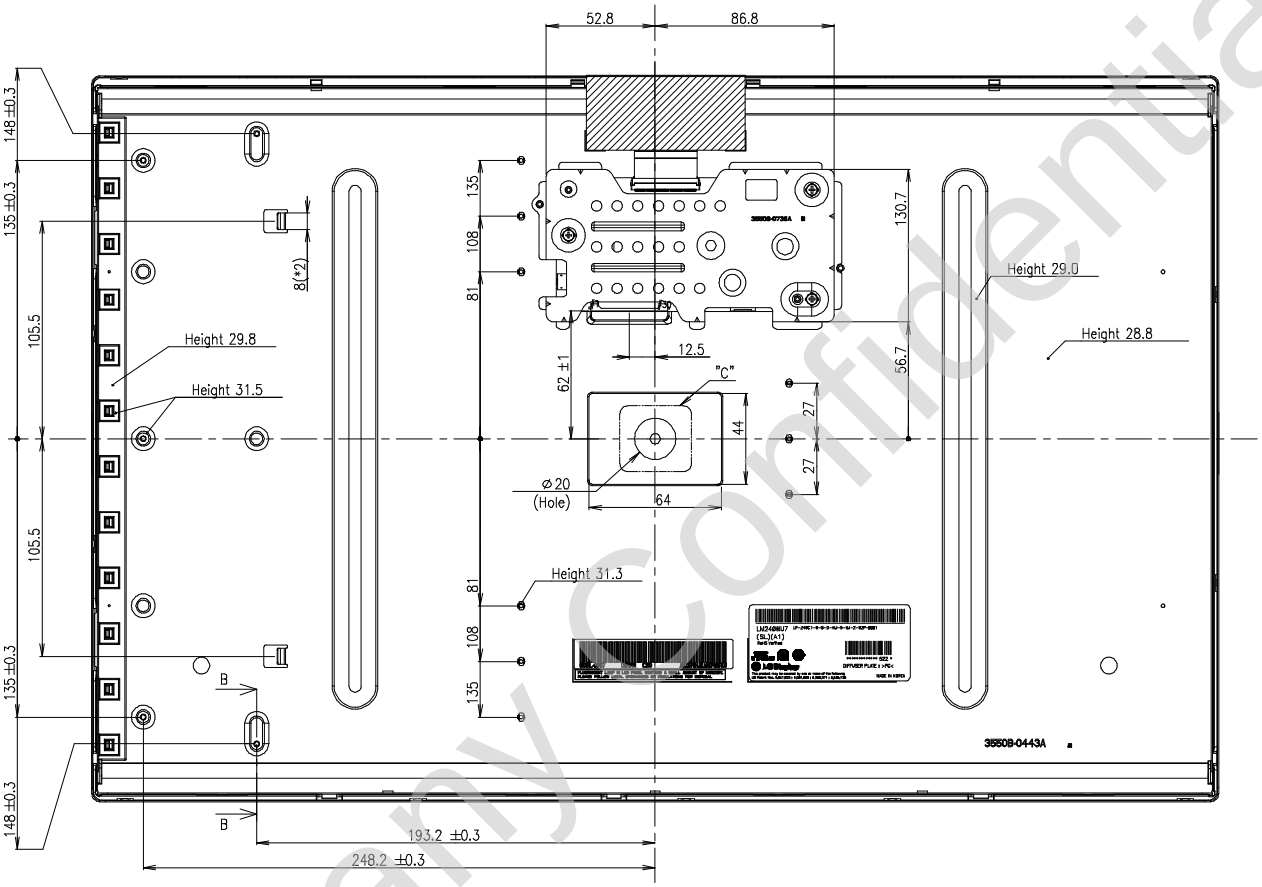
Outline Dimension	Horizontal	546.4mm
	Vertical	352.0mm
	Depth	35.7mm
Bezel Area	Horizontal	522.4mm
	Vertical	328.0mm
Active Display Area	Horizontal	518.4mm
	Vertical	324.0mm
Weight	2830 g(Typ) / 2970 g(Max)	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer	

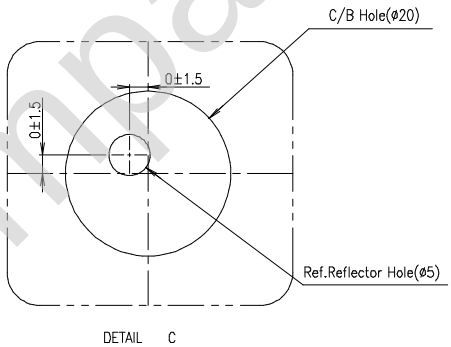
Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.

<FRONT VIEW>



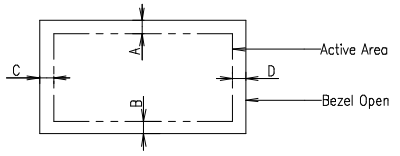
<REAR VIEW>





DETAIL C

Notes
1. Unspecified tolerances are to be ±0.5mm.
2. Tilt and partial disposition tolerance of display area are as following.
(1) Y-direction : IA-BI ≤ 1.4mm
(2) X-direction : IC-DI ≤ 1.4mm



3. Torque Spec of User Mounting : 6.0 ~ 7.0kgf cm
4. I/F Connector Specification : IS100-L30B-C23(UJU) or compatible.

6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude storage / shipment	0 - 40,000 feet(12192m)
9	Maximum Storage Humidity for 4 corner light leakage Mura.	Max 70%RH , Ta=40℃

7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,
Standard for Safety of Information Technology Equipment.
b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,
Standard for Safety of Information Technology Equipment.
c) EN 60950-1:2001, First Edition,
European Committee for Electrotechnical Standardization(CENELEC)
European Standard for Safety of Information Technology Equipment.

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998
(Including A1: 2000)

7-3. Environment

a) RoHS. Directive 2002/95/EC of the European Parliament and of the Council on the reduction of the use of certain hazardous substances in electrical and electronic equipment. January 2003

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A,B,C : SIZE(INCH)
D : YEAR
E : MONTH
F ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

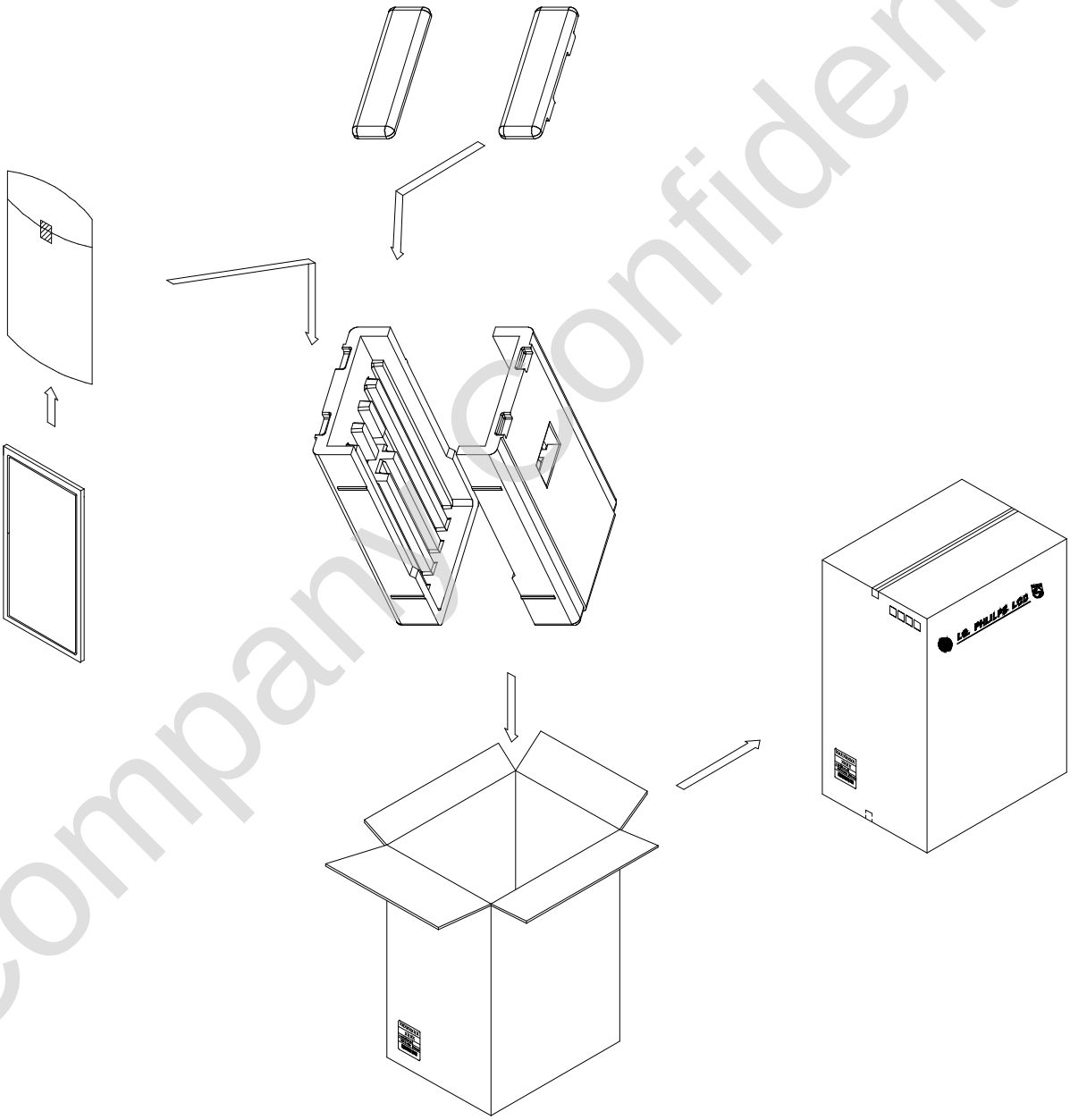
b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

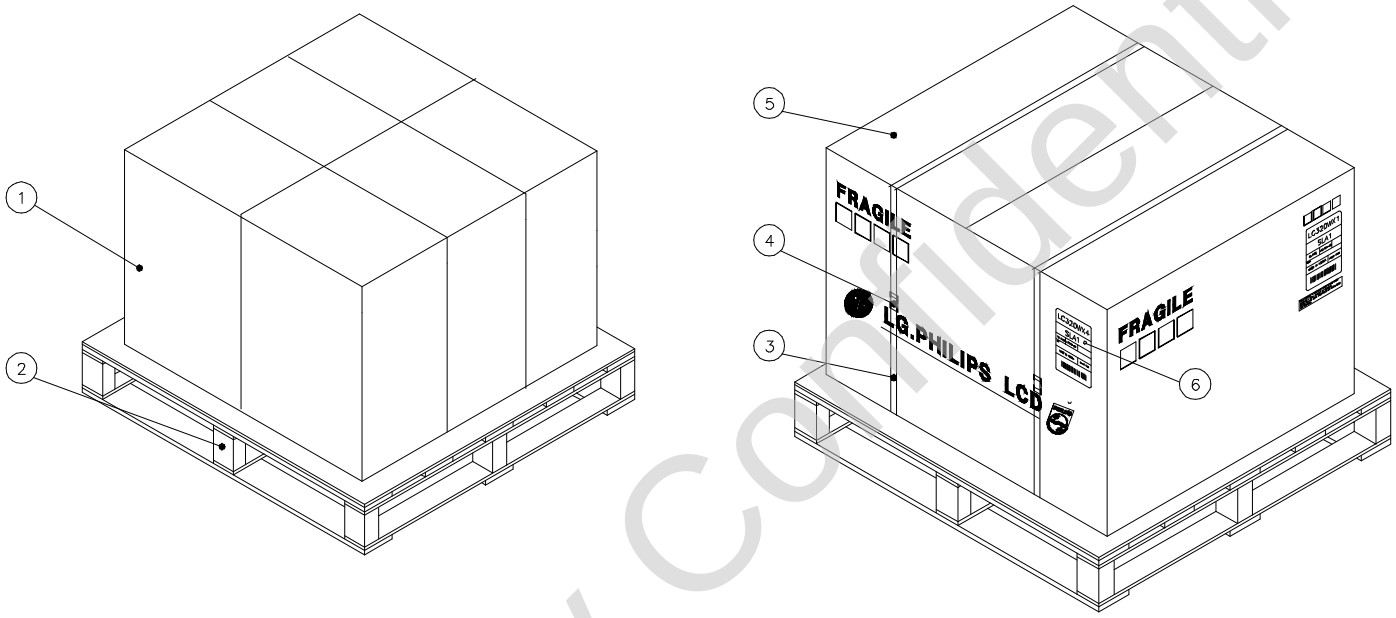
8-2. Packing Form

a) Package quantity in one box : 5EA

b) Box Size : 436 X 346 X 628



8-3. Pallet Form



NO.	DESCRIPTION	MATERIAL
1	PACKING ASS'Y	
2	PALLET	Paper_1030X870X130
3	ANGLE, PACKING	SWR4
4	LABEL	YUPO PAPER
5	TAPE	OPP
6	BAND	PP
7	BAND, CLIP	CLIP 18MM

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V=±200mV(Over and under shoot voltage)
(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
(7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
(8) A screw which is fastened up the steels should be a machine screw.
(if not, it causes metallic foreign material and deal LCM a fatal blow)
(9) Please do not set LCD on its edge.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
(3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.